

Service Manual

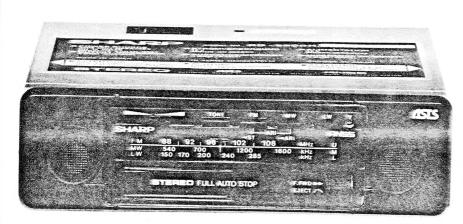


PHOTO: RG-5900H

ATSM580037CST



"In the interests of user-safety the set should be restored to its original condition and only parts identical to those specified be used."

Solid State In-dash Type Cassette Car Stereo Player with LW/MW/FM/FM Stereo Radio and APSS

MODEL RG-5900H (With ARI) RG-5900E

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SPECIFICATIONS

GENERAL

Type:

Solid State In-dash Type 4-Track 2-

channel Full Auto Stop Cassette Car Stereo Player with built-in LW/MW/FM/

FM STEREO/ARI 3 band Radio.

RG-5900E: without ARI

Power source:

12V (for negative earthing car only)

Output impedance: 4 ohms/channel

Semiconductors:

RG-5900H: 17-transistor (1-FET), 15

diode (2-LED) and 7-IC

(integrated circuit)

RG-5900E: 10-transistor (1-FET), 5-

diode (1-LED) and 5-IC

(integrated circuit)

Output power:

8W + 8W (maximum power)

5W + 5W (at 10% distortion)

S/N:

Dimensions:

178(W) × 138(D) × 44(H) mm

Weight:

1.3 Kg

TAPE PLAYER SECTION

Playback system:

4-track, 2-channel Stereo

Using tape:

Philips standard compact cassette tape

Tape speed: Wow and flutter: 4.75 cm/sec.

0.3% (DIN 45 511) Frequency response: $50Hz \sim 10kHz/-6dB$

Fast forward/

Rewind time:

120 seconds (@ C-60 cassette tape)

Motor:

D.C. motor with medhanical governor

RADIO SECTION

Frequency range:

LW 150 ~ 285kHz

MW 520 ~ 1,620kHz FM 87.6 ~ 108MHz

LW/MW 452kHz

FM 10.7MHz

Sensitivity:

LW 400μV/20dB

MW $40\mu V/20dB$

FM 2.5μV

Specifications are subject to change without prior notice.

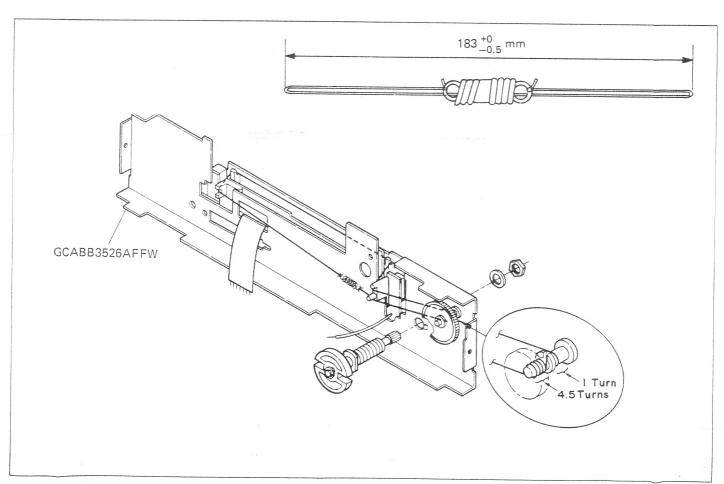


Figure 2-1 DIAL CORD STRINGING

PARTS LAYOUT

1 Balance Control Knob (JKNBP0103AFSA)
2 Tone Control Knob (JKNBM0345AFSA)
3 Band Selector Knob (JKNBM0345AFSA)
4 Aerial Trimmer (TCT) (RTO-A1057AFZZ)
5 Power ON-OFF/Volume Control Knob (JKNBK0201AFSA)
10 Tuning Control Knob (JKNBM0345AFSA)
10 Tuning Control Knob (JKNBM0345AFSA)
10 Tuning Control Knob (JKNBK0201AFSA)

Figure 3-1 FRONT PARTS LAYOUT

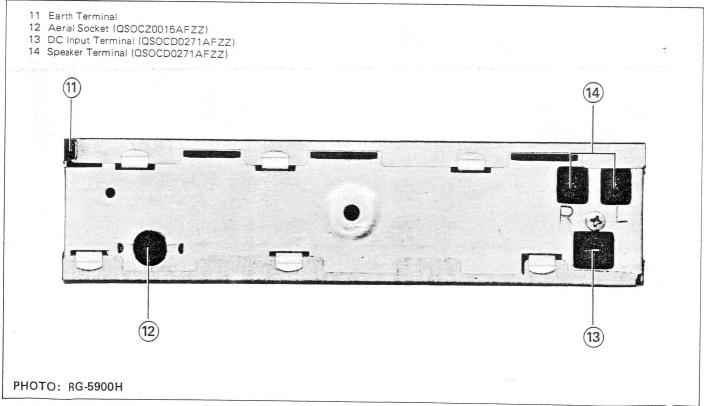


Figure 3-2 REAR PARTS LAYOUT

DISASSEMBLY

- 1. Remove the Bottom cabinet, then take it out. (See Fig. 4-1)
 2. Remove the nose piece, then take it out. (See Fig. 4-2)
 3. Remove the five screws retaining the Mechanism chassis at printed wiring board. (See Fig. 4-4) 4. Pull out the two wiring connecting sockets provided on the

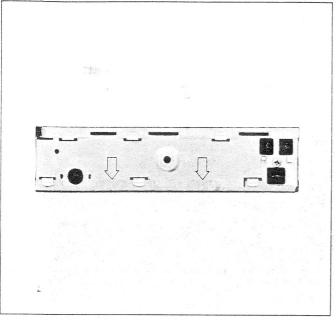


Figure 4-1

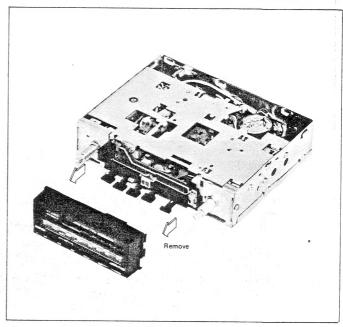


Figure 4-2

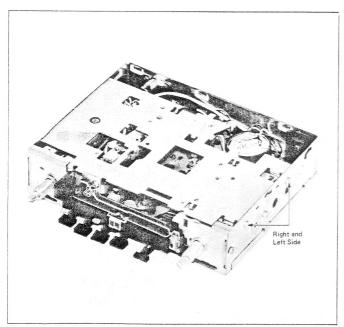


Figure 4-3

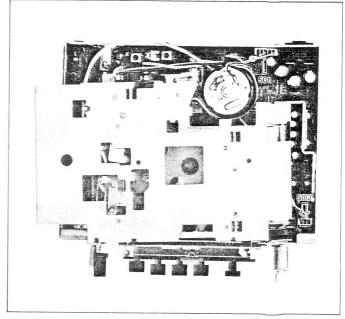


Figure 4-4

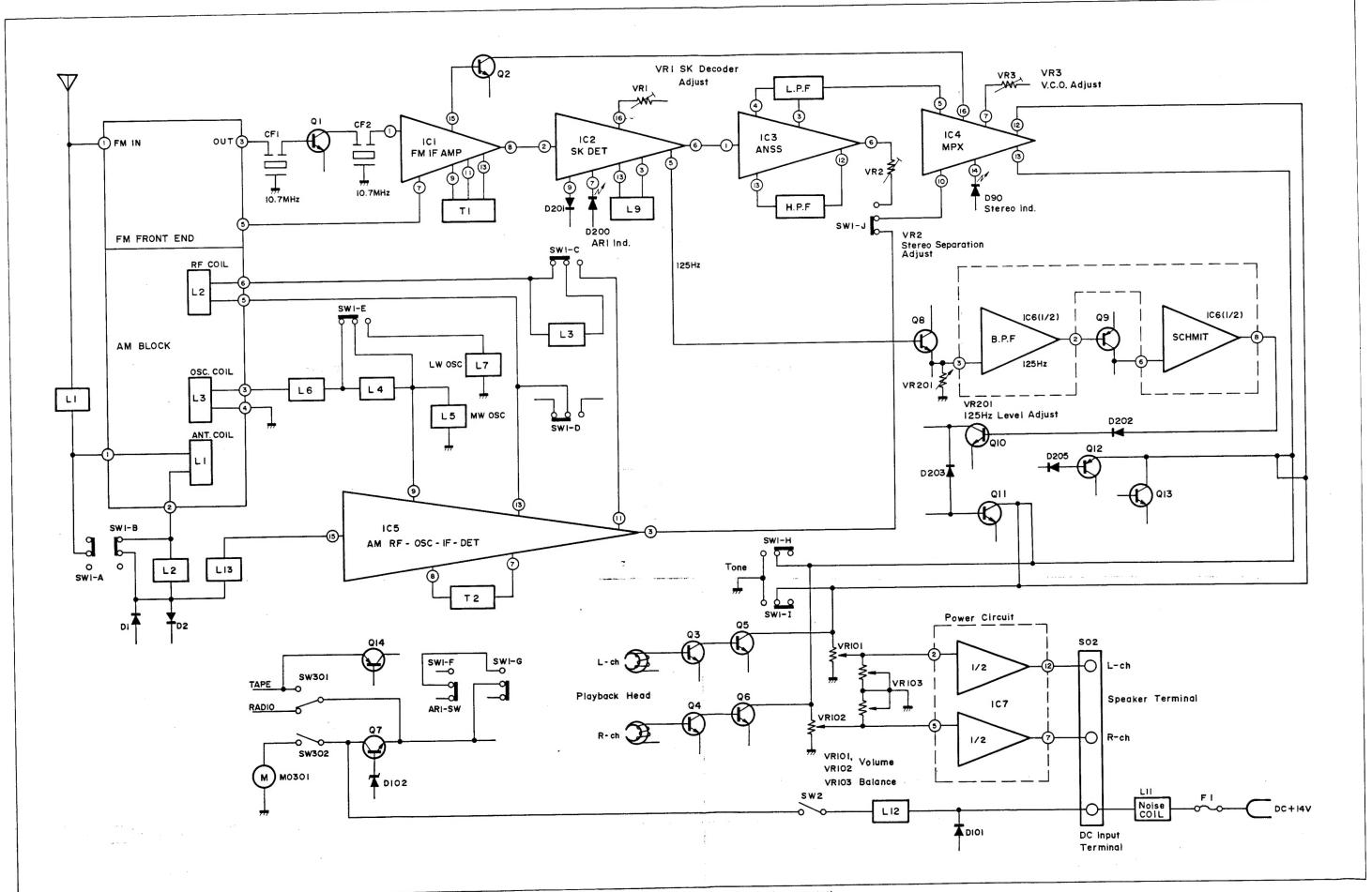


Figure 5 BLOCK DIAGRAM (RG-5900H)

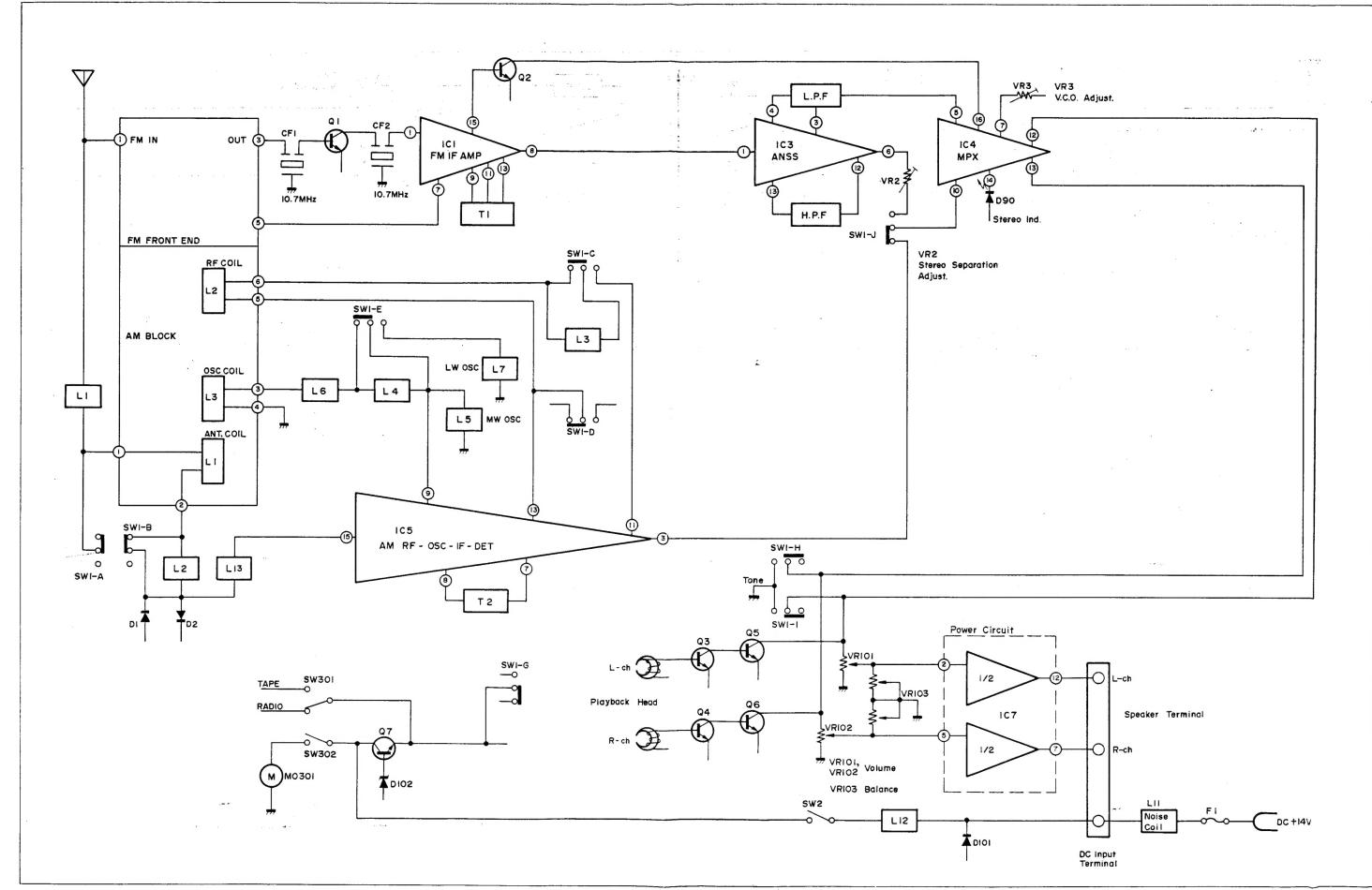


Figure 7 BLOCK DIAGRAM (RG-5900E)

ANSS (Automatic Noise Suppressor System)

SUMMARY

Electrical interferences generated by cumbustion engines used in motor-cars are necessary to be suppressed to make listening to FM broadcastings possible. An effective way to suppress interferences produced by its own car and those of others received via the antenna is to apply a kind of noise gating for the output signal of the FM demodulator.

Since the mentioned interferenced have a frequency spectrum upto several hundreds of kHz being easily reproduced by the FM demodulator there is sufficient signal available beyond 53kHz to drive this gating circuit. Based upon these principles the ANSS has been devoloped.

INTRODUCTION

In the FM car radio, pulse noise received via the antenna becomes unpleasant noise that interferes with the happy FM listening, passing the circuits between the antenna and the speaker. The ANSS is a device that can automatically remove such pulse noises from the incoming signals, so only the desired signals will be obtained. Being detected at the FM detector, both the desired signal and pulse noise, caught by the antenna, are superposed each other as shown in Figure 9-1. Then they are applied to the ANSS circuit where only the desired signal is developed since the noisy one is removed.

The bandwidth of the ANSS, necessary for a good stereo signal, has to be about:

38 kHz + 15 kHz =53 kHz. (Stereo subcarrier) (Upper side band channel)

For stereo signal reception, the arriving signals are applied to the gate circuit of the ANSS, in order to prevent the pilot signal from undergoing amplitude modulation (which causes noisy sound through the succeeding circuits), this pilot signal is first supplied to the 19 kHz trap filter, located prior to the gate circuit, where it is removed and only the audio signal can appear at the ANSS circuit then to be applied to the stereo multiplex circuit.

In addition, before being supplied to the 19kHz trap filter, a part of the stereo pilot signal is also applied to the VCO circuit, a part of the stereo multiplex circuit. Since the VCO circuit is of PLL system, if the pilot signal enter the VCO circuit, the PLL becomes completely locked so as to eliminate any possiblity of noise occurrence in the stereo multiplex circuit due to the noise entered together with the pilot signal. In this way pulse noise caught by the antenna is eliminated.

Another feature of this system is that in FM stereo reception, the signal to noise ratio is improved, because the stereo pilot signal has no possibility of mixing in the audio signal produced, being removed by the 19 kHz trap filter.

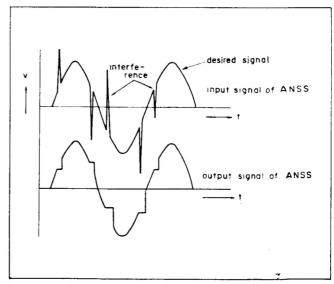


Figure 9-1

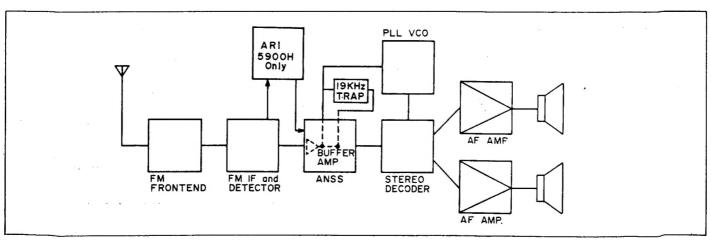


Figure 9-2 BLOCK DIAGRAM

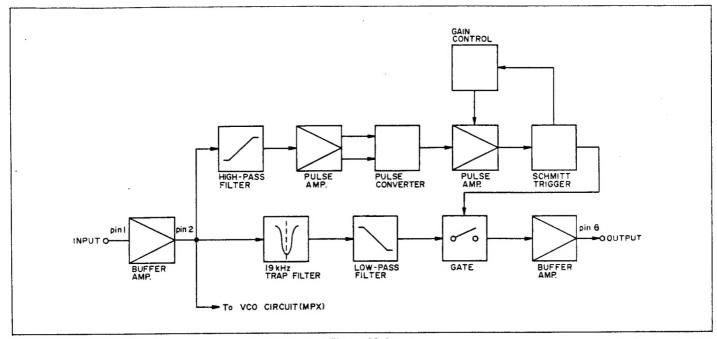


Figure 10-1

Explanation of the block diagram

Input signals at the pin 1, both the destred signal and pulse noise are appeared at the pin 2 via the buffer amplifier. Then, they are divided into the two, one to be applied to the high-pass filter side and another to the low-pass filter side.

In the high-pass filter, only pulse noise is picked out from the incoming signal, and this noise is amplified by the pulse amplifier. The noise thus amplified is transferred to the pulse converter where the negative pulse is converted to positive one to be supplied to the pulse amplifier where it is formed a strong signal enough to activate the Schmitt trigger.

Coming out of the Schmitt trigger, the signal is coupled to the gate circuit of the ANSS, which will be turned off. Also, the ANSS is equipped with the gain control circuit that will control the input signal of the Schmitt trigger, if a great amount of the continual pulse noises arrived, and prevent the gate circuit from turning off.

Meanwhile, in the low-pass filter side, the arriving signal is first applied to the 19 kHz trap filter where the stereo pilot signal is removed, and the remaining signal is coupled to the low-pass filter. The signal coming out of the low-pass filter, which has frequencies lower than 53 kHz, is then applied to the gate circuit. In this gate circuit, pulse noise, if being included in the input signal, will be got rid of and so only the desired signal will be developed.

However, being turned off, the gate circuit has no output. To prevent this, the ANSS is equipped with such a circuit that maintains output at the level just before the gate circuit is turned off. For this reason, there will be no secondary noise appe

secondary noise appearance caused by switching of the gate circuit.

It is noted that a part of the stereo pilot signal is, without entering the 19 kHz trap filter, coupled to the VCO circuit (of the stereo multiplex circuit) to drive.

DESCRIPTION OF THE CIRCUIT

Input stage

The input stage consists of a simple emitter follower, see Fig. 10-2.

This stage has been added to the circuit in order to avoid an influence of the input impedance of the L.P. and H.P. filters on the output of the FM detector and reversed. To be sure that the circuit works correctly, the DC voltage at pin 1 needs to be 0.4 x V_7 - V_{14} (0.4 x supply voltage). The input impedance at 1 kHz: $|Zi| \ge 30$ K ohms.

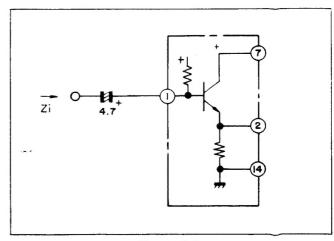


Figure 10-2

The low-pass filter (delay line)

To be sure of a good signal handling of the desired signal this filter has to meet next requirements.

- a) the delay time has to be at least $3 \mu sec.$
- b) the amplitude characteristic has to be as flat as possible in the pass-band.
- c) the phase behaviour has to be linear.
- d) the distortion of the desired information at the output must be as low as possible.

In order to meet these requirements use is made of a 4th order Butterworth filter realised by an active RC circuit. (see Fig. 11–1).

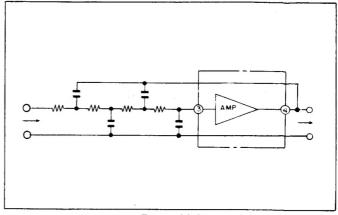


Figure 11-1

Gate circuit and output amplifier

The circuit is given in Fig. 11-2.

The point, indicated with P, is connected to the positive output of the Schmitt-trigger.

If there is a positive pulse at P then Qc becomes conducting and takes away the driving current for Qb. At the same time the base voltage of Qe will be kept constant by the RC circuit connected to pin 5.

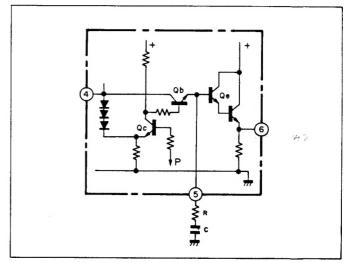


Figure 11-2

High pass filter

19 kHz filter

In order to detect the interferences out of the input signal a high pass filter is used.

In practive one wants to suppress as much interferences as possible in order to get a "clean" output signal.

The theorical curve of the H.P. filter has been given in Fig. 11—3.

A practical approximation of this curve can be achieved by a 4th order Chebyshev filter at which for car radio applications —3dB can be chosen at 91kHz.

To get a steep slope an extra R and C are added circuit.

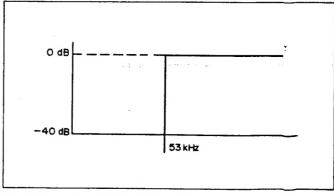


Figure 11-3

signal will look like Fig. 11—4. To be sure of no audible low-frequency component, the voltage during suppression needs to be zero. (See gap Fig. 11—4) However this happens only very sporadic so that filtering out of the undesired low frequency component is necessary, otherwise this low frequency component breaks through to the audio part via the MW-channel. Thus a 19kHz filter is added to the circuit.

During suppression but without this filter the 19kHz

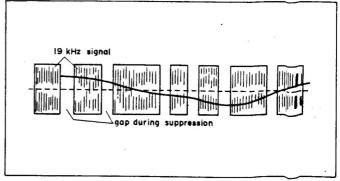


Figure 11-4

Gain control

The circuit is given in Fig. 12-1.

To be sure of an audible signal during a too high repetition rate of the interference pulses and/or a too intensive noise it is necessary to reduce the repetition rate of the suppression.

From the Schmitt-trigger the negative output pulses are fed to the integrating network connected to pin 10.

If Vc'' which is V7-10 becomes $\geq VBEQ_8$ then the gain of the pulse amplifier will be reduced.

In case of noise, at which normally the "interference spikes" are very close to each other, it is better to build-up the voltage across C" directly, because during one suppression time there are a lot of noise spikes.

This information for the gain control is lost if the negative output of Schmitt-trigger is used.

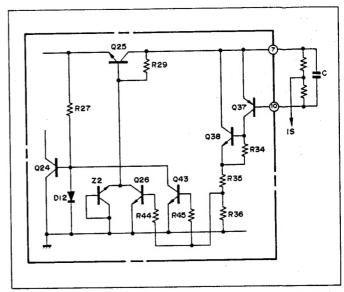


Figure 12-1

Schmitt-trigger

The circuit is shown in Fig. 12-2.

The positive output is used for driving the gate circuit while the negative output is fed to the gain control.

The pulse width of the pulses delivered by the Schmitt-trigger can be controlled by an RC network at pin 9 of Fig. 12-2.

The pulse-width as function of the value of the C° connected at pin 9 while the R° is kept constant at 6.8K, is given in Fig. 12-3.

For measurements the pulse at the input of the ANSS (pin 1) has a pulse width of 10 μ sec., a rise time of 6 μ sec. and a pulse hight of 0.1 V.

To ensure proper operation of the Schmitt trigger for various R°C° combinations it is advised to measure the pulse at pin 1 and pin 8.

The depicted signals should have a shape as shown in Fig. 12–4.

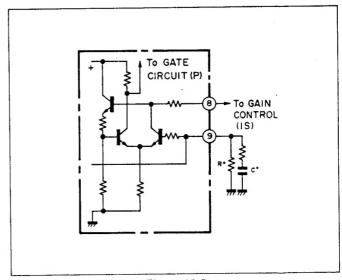


Figure 12-2

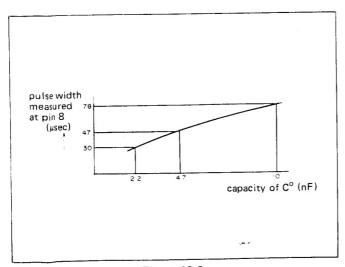


Figure 12-3

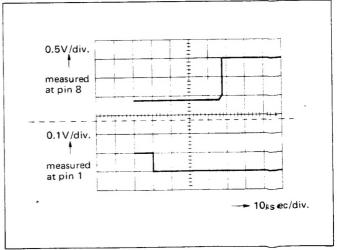


Figure 12-4

■ ARI System

The ARI circuit of the RG-5900H is activated when the band selector switch is set to "ARI" position — this position is actually achieved by pushing on its FM and MW buttons at a time — and it provides the following functions.

- 1) Unless the unit tunes in any SK station, the sound is kept muted (because of the action of IC2).
- 2) When the unit begins tuning in an SK station (without DK single included), the sound comes to be alive (because of the action of IC2 too).
- 3) When the unit begins tuning in an SK signal (now with DK signal present), it becomes possible to listen to the traffic information. Even if the volume control has been set at
- "MIN" position, the traffic information can still remain as loud as you can hear it. And even when the unit is playing the tape, the traffic information can be heard suppressing the tape play, which is enabled by the integrated circuit IC6 and the transistors Q8, Q9, Q10, Q11 and Q14.
- 4) About 20 seconds after the SK signal has disappeared, the alarm comes out its output is of approx. 1 kHz sine wave.

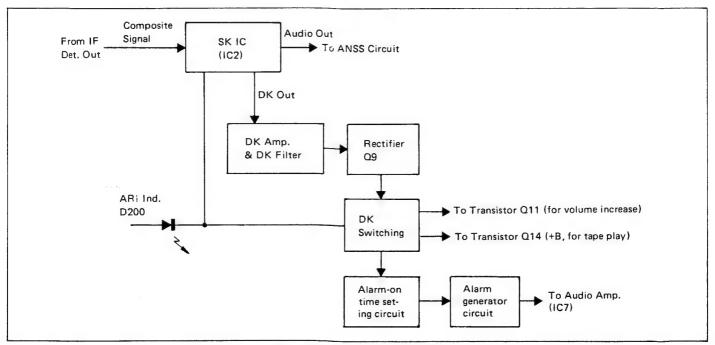


Figure 13-1 BLOCK DIAGRAM

■ SK Signal Control Circuit

The integrated circuit IC2 is aimed at controlling the SK signal and its structure is as shown in Fig. 14-1. The important functions of this circuit are found at its included two pins, pin 9 for the muting and pin 12 for V.C.O. control.

- 1) When the unit is tuning in ARI band, the V.C.O. becomes active to have the unit operate as follows:
 - a) With pin 9 opened;
 In this case, only if there arises SK signal (57 kHz) at the input of pin 2, does the audio signal come out of pin 6.
 - b) With pin grounded; In this case, the audio signal always comes out of pin 6 even if the SK signal is absent at pin 2.
- 2) When the unit is tuning in other than ARI band:

 Then, the V.C.O. stops to operate so that the audio signal becomes alive whether pin (9) is opened or grounded.

 The electrolytic capacitor C201 in connection with pin (9) is for the purpose to decide the muting time constant (t₁) just when the SK signal turns "off" (see Fig. 13-2).

 The output of pin (7) becomes "Low" level when given SK signal and "High" level when not given SK signal, and it also becomes "High" level when the V.C.O. ceases to operate.

Pin 5 is the output pin where the BK and DK signals come out.

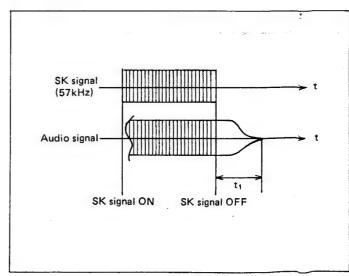


Figure 13-2

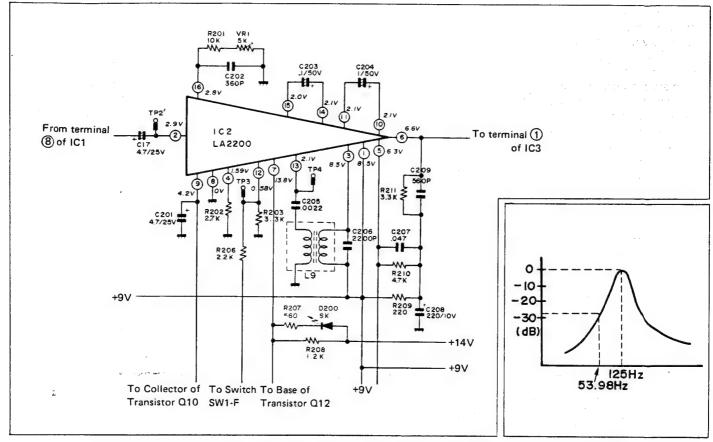


Figure 14-1 SK SIGNAL CONTROL CIRCUIT

Figure 14-2

■ DK Control Circuit

See Fig. 14-3.

The DK control circuit is composed of an impedance converter (Q8), a band-pass filter (IC6, R214, R217, C210, C211 and VR201), a rectifier circuit (Q9) and Schmidt circuit (IC6). The impedance converter is to convert the impedance of the BK and DK signals which are gathered at pin \bigcirc 5 of IC2; the band-pass filter to separate only the DK signal from the mixture (of DK and BK signals); the rectifier circuit to turn the DK signal (of alternative current) into a direct current signal.

Let's consider in detail the action of this circuit:

The transistor Q8 is made an emitter follower circuit whose value is to activate the next band-pass filter even with its input impedance kept as low as possible, so that the BK/DK signal

filtered out will have its impedance as low as possible too. Consisting of the capacitors, resistors and an operational amplifier, the band-pass filter provides such characteristic as shown in Fig. 14-2: the BK signal is attenuated by a pprox. 28 dB; its output signal is of 125 Hz which appears at pin ② of the operational amplifier IC6, and whose voltage is made constant (8.5 V) by the resistors R215 and R216. The output signal, then, enters the rectifier circuit (Q9), and the potential at its collector becomes "High" level only if there exists the DK signal to come here; therefore, the potential at pin 6 of IC6 also goes up to "High" level, and the output signal now present at pin 8 at "Low" low level before, becomes "High" level as well.

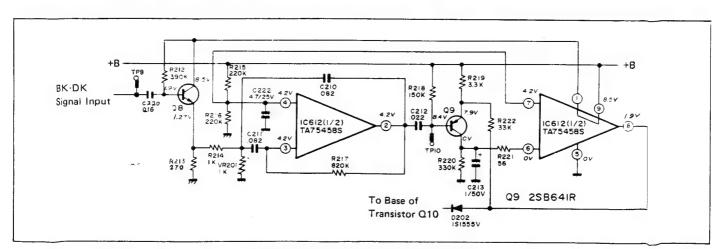


Figure 14-3 DK CONTROL CIRCUIT

How about DK switching

When the DK signal arrives at pin 6 of IC6, the output of pin 8 gets "High" level to turn on the transistor Q10, which results across the diode D206 in that the preamplifier (Q3 and Q4) turns off even if the tape/radio selector switch (SW301) has been set at "TAPE" position. As a result the tape-played sound is muted and at the same time the transistor Q14 is turned on to apply +B voltage to the multiplex IC (IC4) allowing the unit to make the radio

signal (traffic information) alive. The traffic information is kept loud enough for us to hear it even if the volume control has been set at "MIN" position because that: if the transistor Q10 turns on first, then this causes the transistor Q11 to be off so that the output of IC4 can couple directly with pins 2 and 5 of the power amplifier IC7 without passing through the volume control (set at "MIN" position) at all.

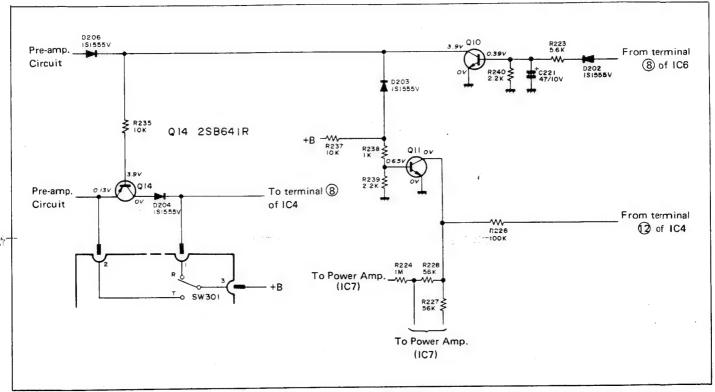


Figure 15-1 DK SWITCHING

Alarm Circuit

The moment the SK signal comes dead, the output of pin 7 of IC2 becomes "High" level and so the transistor Q12, about 20 seconds later, turns off through the diode D205 — this time constant is decided by the electrolytic capacitor C215 and resistor R229. The CR oscillator which consists of transistor Q13, resistor and capacitor, then, gets active to produce a signal of approx. 1 kHz letting the alarm go on.

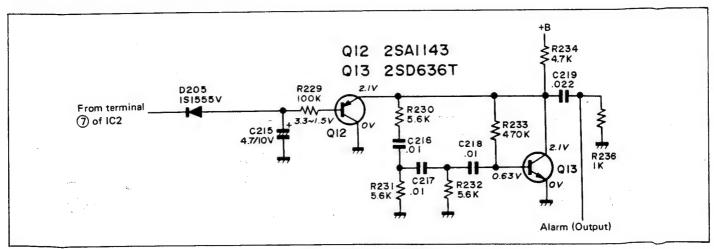


Figure 15-2 ALARM CIRCUIT

MECHANICAL ADJUSTMENT

Checking of Tape/Radio Selector Switch Performance

- 1. When the cassette tape is ejected, check that this switch is put off.
- 2. When a dummy cassette is loaded, check that this switch can turn on.

At the time push the dummy cassette slowly to enable PLAY mode, and see that the subchassis can't move down unless the cassette goes beyond the lock pin position (shown in the figure).

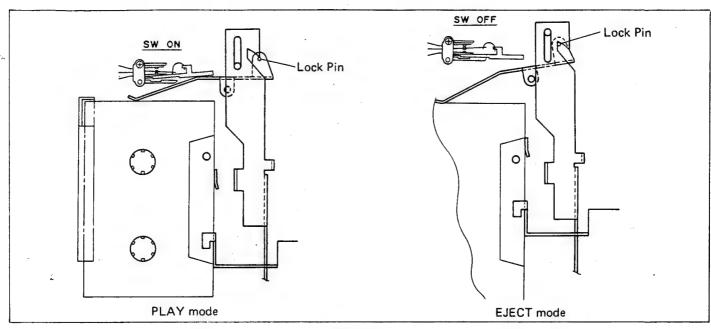


Figure 16-1

Flywheel Thrust Clearance Adjustment

1. When the flywheel is set in place, check if its thrust clearance is within 1.0 mm to 0.4 mm. If hot, correct it by bending the flywheel's bracket.

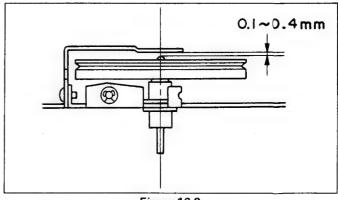


Figure 16-2

Checking of Motor ON/OFF Switch Performance

1. When the cassette tape is ejected, this switch should be off; when the cassette tape is played, it should be on. And in both EJECT and PLAY modes, the clearances shown in the figure should be as specified.

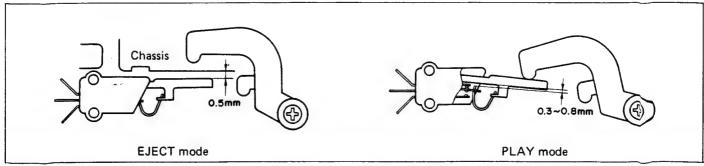


Figure 16-3

Clearance Checking

- 1. Under PLAY mode, check that the flywheel and fast forward roller move properly to produce a specified clearance between them.
- 2. Under FF mode, check that the take-up turntable and play idler move properly to create a specified clearance between them. Also check for the clearance between the fast forward roller lever and operation lever.

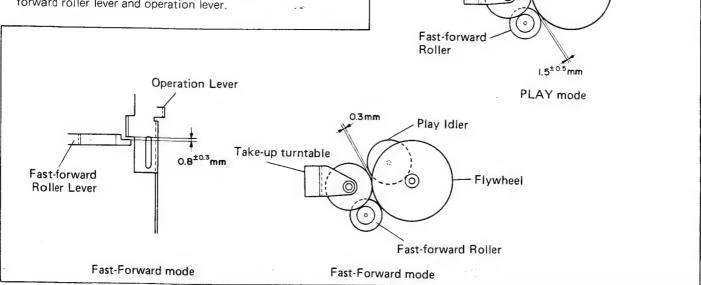
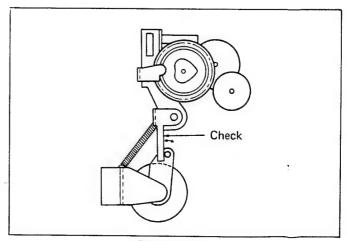


Figure 17-1

Checking of Auto Stop Operation

1. The sensor lever should be able to move in either direction as shown in the figure.



Play Idler

Flywheel

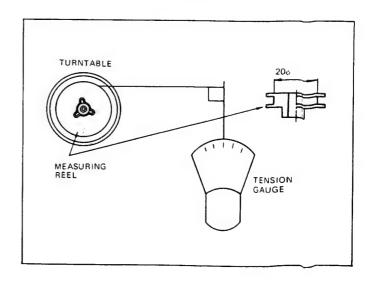
Take-up turntable

Figure 17-2

TORQUE CHECK

- 1. Set the torque measuring reel to the turntable (the take-up side at play or fast forward mode).
- 2. Then, rotate the reel in the same direction as for turntable and read the torque value when the pointer is stabilized.

Mode	Torque value
Play	35 — 55 gr.cm
Fast Forward	75 — 110 gr.cm



Checking of Tape Speed

- 1. Connect a frequency counter to the speaker terminal.
- 2. Using a test tape (MTT-111, 3 kHz), play it for 10 seconds at its beginning and end parts.
- Check, then, that the playback frequency indicated by the counter is 2910 to 3090 Hz at maximum. If not, renew the motor.

Note:

The supply voltage is set at DC14V, and the unit must be kept horizontal during the measurement.

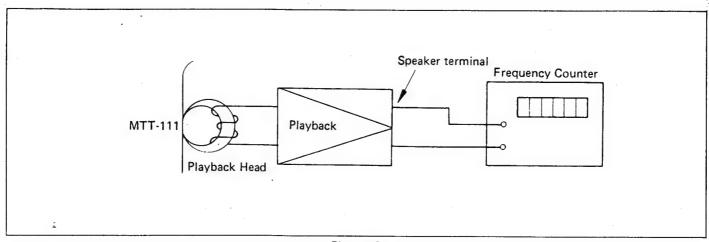


Figure 18-1

HEAD AZIMUTH ADJUSTMENT

Standard Test Tape to be applied: Philips HU-71512 or the equivalent (TEAC MTT-113, VICTOR VTT-601).

- (1) Set the Player Unit on.
- (2) Turn the azimuth adjusting screw until the output of the test tape (6.3kHz) is boosted up to the maximum.

Caution: After completion of the adjustment, be sure to lock the adjusting screw in place, using glyptal or glue.

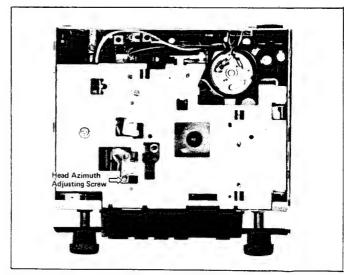
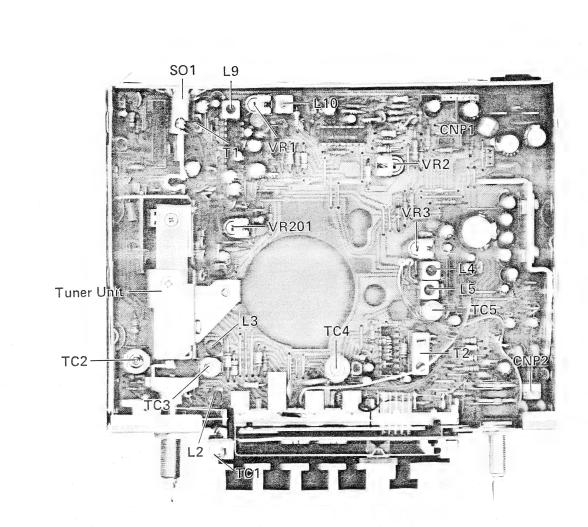


Figure 18-2



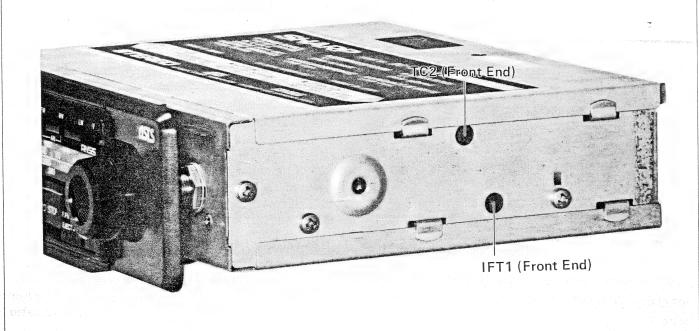


Figure 19 ALIGNMENT POINT

Should it become necessary at any time to check the alignment of this receiver, proceed as follows;

- 1) Connect an output meter across the speaker voice coil lugs.
- 2) Set the volume control at maximum.
- 3) Attenuate the signals from the generator enough to swing the most sensitive range of the output meter.
- 4) Use a non-metallic alignment tool.
- 5) Repeat adjustments to insure good results.

LW/MW ALIGNMENT CHART

Set the band selector switch at "MW" or "LW" position.

STEP 1 2 3 4 5 6 7			SIGNAL GEN	ERATOR	RECE	IVER	
STEP	BAND	TEST STAGE	CONNECTION TO RECEIVER	INPUT SIGNAL FREQUENCY	DIAL SETTING	REMARKS	ADJUSTMENT
1	MW	IF	Connect signal generator through a dummy to the antenna socket (SO1) Ground lead to the receiver chassis. (Refer to Figure 20)	Exactly 452kHz (400Hz, 30%, AM modulated)	High end of dial (minimum inductance)	Adjust for maxi- mum output on speaker voice coil lugs.	T2
.2	MW	IF .	Repeat until no further	improvement can be r	made,		
	i mara i i di		Same as step 1.	Exactly 510kHz (400Hz, 30%, AM modulated)	Low end of dial (maximum inductance)	Same as step 1.	Adjust the MW oscillator coil L5.
3	MW	Band Coverage	Same as step 1.	Exactly 1650kHz (400Hz, 30%, AM modulated)	High end of dial (minimum inductance)	Same as step 1.	Adjust the MW oscillator trimmer TC5.
4	MW	Tracking	Same as step 1.	Exactly 1400kHz (400Hz, 30% AM modulated)	1400kHz.	Same as step 1.	Adjust the MW antenna trimmer TC1, and then adjust the MW RF trimmer TC3.
5	MW		Repeat steps 3 and 4 ur	ntil no further improve	ement can be made.		
			Same as step 1.	Exactly 145kHz (400Hz, 30%, AM modulated)	Low end of dial (maximum inductance)	Same as step 1.	Adjust the LW oscillator trimmer TC4
6	LW	Band Coverage	Same as step 1.	Exactly 310kHz (400Hz, 30%, AM modulated	High end of dial (minimum inductance)	Same as step 1.	Adjust the LW oscillator coil L4.
			Same as step 1.	Exactly 160kHz (400Hz, 30%, AM modulated)	160kHz	Same as step 1.	Adjust the LW antenna trimmer TC2.
7	LW	Tracking	Same as step 1.	Exactly 260kHz (400Hz, 30%, AM modulated)	260kHz.	Same as step 1.	Adjust the LW antenna coil L2, and then adjust the LW RF coil L3.
8	LW		Repeat steps 6 and 7 u	ntil no further improv	ement can be made.		
0	vv		Tropeat stops o and 7 d	THE TOTAL THE TRADE			

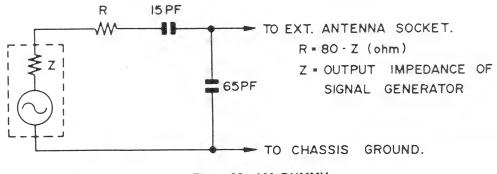


Figure 20 AM DUMMY

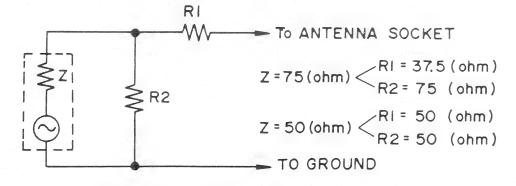
FM ALIGNMENT CHART

Set the band selector switch at "FM" position.

-						
		SIGNAL GENER	ATOR	RE	CEIVER	
STEP	TEST STAGE	CONNECTION TO RECEIVER	INPUT SIGNAL FREQUENCY	DIAL SETTING	REMARKS	ADJUSTMENT
1	IF (NOTE B)	Connect signal generator through a .022MFD capacitor to antenna socket (SO1). Connect generator ground lead to the receiver chassis.	Exactly 10.7MHz (400Hz, 30%, FM modulated)	Low end of dial. (maximum inductance)	Connect VTVM between test point TP1 and chassis ground.	Tune IFT 1 (Front End)
2	/ Orature Same as step		Exactly 10.7MHz (unmodulated)	Same as step 1.	See NOTE A.	See NOTE A.
3	Repeat step	os 1 until no further improvement c	can be made.			
4	Band Coverage	Connect signal generator through a dummy including output impedance of signal generator to the car antenna socket (SO1). Ground lead of generator connected to the receiver chassis. (Refer to Figure 26)	Exactly 87.2 MHz (400Hz, 30%, FM modulated)	Same as step 1.	Adjust for maximum output at speaker voice coil.	Oscillator trimmer TC2

NOTE A

- 1) Connect VTVM (10 volt range D.C. Scale between test point TP2 and Pin 13 of IC-1.
- 2) Adjust T1 for 0 volt on VTVM.



Z=OUTPUT IMPEDANCE OF SIGNAL GENERATOR

Figure 21 FM DUMMY

NOTE B

Five kinds of ceramic filter (CF1, CF2) are available for this set. The difference of central frequency from each other can be known by the color indication. The table below shows such a difference of IF and S curve, depending upon the color indications of the ceramic filter (CF1, CF2).

	D	Black	10.64MHz ± 30kHz
Control	В	Blue	10.67MHz ± 30kHz
Central	A	Red	10.70MHz ± 30kHz
Frequency	C -	Orange	10.73MHz ± 30kHz
	E	White	10.76MHz ± 30kHz

For their employment, it is required to use two ceramic filters of same type.

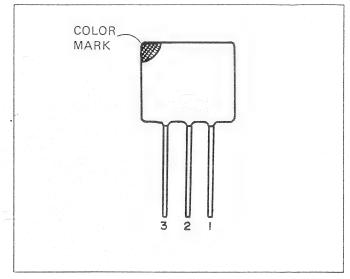


Figure 22-1

FM STEREO ALIGNMENT

Set the band selector switch at "FM" position.

	a SIGNAL GE	NERATOR		RECEIVER		ADJUSTMENT	
STEP	CONNECTION TO RECEIVER	INPUT SIGNAL FREQUENCY	DIAL SETTING	REMARKS	METER CONNECTION		
1			98MHz	Adjust so that the frequency becomes 19.0kHz. (In case an oscilloscope is connected to the test point TP8, adjust the signals to be 19kHz by using Lissajou's wave-form).	Connect the frequency counter (or oscilloscope) through a 100K ohm resistor to TP8 (3 pin of IC4).	VR3	

If without the frequency counter, proceed with the alignment as follows. While receiving a FM stereo signal, turn the VR3 until the P.L.L. will be locked (when it is locked, the stereo indicator will be lit). Then, reversely turn the VR3 halfway and fix it.

ANSS ADJUSTMENT

- 1. Set the band selector switch at "FM" position.
- 2. Apply a 19 kHz signal of 30 mV to (TP5).
- 3. Connect a VTVM and/or an oscilloscope to (TP7).
- 4. Adjust L10 for minimum output at (TP5).
- 5. Then, apply a 1 kHz signal of 100 mV to (TP5).
- 6. Make sure that there is no output at pin 6, applying a 100 kHz signal of 50 mV further to pin 13.
- 7. Next, make sure that a 1 kHz signal of 100 mV appears at (TP5), connecting (TP-6) to earthe.

THE INSTRUCTION OF FREQUENCY ADJUSTMENT

In order to comply with Pfg. Nr. 358/1970, please fix the low end of dial frequency (87.5MHz) and the high end of dial frequency (107.9MHz) on FM band, by adjusting oscillation trimmer (TC2) Front End. and oscillation coil (L4), respectively, as illustrated in Figure 19.

ARI ADJUSTMENT (RG-5900H Only)

Set the band selector switch at "ARI" position.

1. 57kHz VCO Adjustment

	RECEIVER		
DIAL SETTING		METER CONNECTION	ADJUSTMENT
	Adjust so that the frequency becomes 57.0kHz. (In case an oscilloscope is connected to the test point TP-3, adjust the signals to be 57kHz by using Lissajour's wave-form.)	Connect the frequency counter (or oscilloscope) through a 10K ohm resistor TP-3. (12 pin of IC-2)	VR1

2. 57kHz Level Adjustment

• Signal generator in use: CR oscillator or ARI SSG

(Standard signal generator) Meter in use: Level meter or oscilloscope

· Connection to receiver:

a. Apply a signal of 57 kHz, 5 mV from the CR oscillator or ARI SSG, across a capacitor of 4.7 μ F, between the test point TP-2' (pin(2) of IC2) and earth.

point TP-2' (pin 2) of IC2) and earth.

b. Connect the level meter or oscilloscope between the test point TP-4 (pin 13) of IC2) and earth.

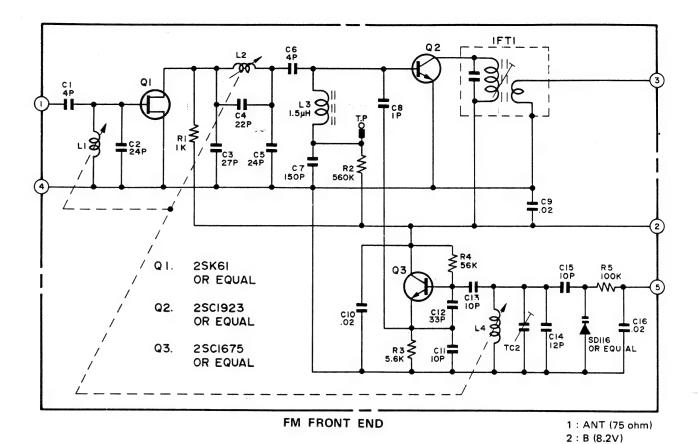
• Adjustment:

Rotate the coil L9 so that the level meter or the oscilloscope swings the most

3. 125Hz Level Adjustment

- Signal generator in use: CR oscillator or ARI SSG
- Meter in use: Level meter or oscilloscope
- · Connection to receiver:
- a. Apply a signal of 125 Hz, 5 mV from the CR oscillator or ARI SSG, between the test point TP-9 and earth.
- b. Connect the level meter or oscilloscope between the test point TP-10 and earth.
- · Adjustment:

Rotate the semi-variable resistor VR201 so that the level meter or oscilloscope swings the most



5 : AFC (4.4V)

NOTES ON SCHEMATIC DIAGRAM

SW1 (A ~ J): Band Selector Switch "MW" position.

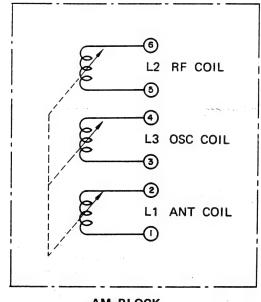
SW2: Power Switch "off" position.

SW301: Tape/Radio Selector Switch "Radio" position.

3: OUT (300 ohm)

4 : E

SW302: Motor Switch "off" position.



中国创新的,通过的国际企业的基础企业企图 经营业税

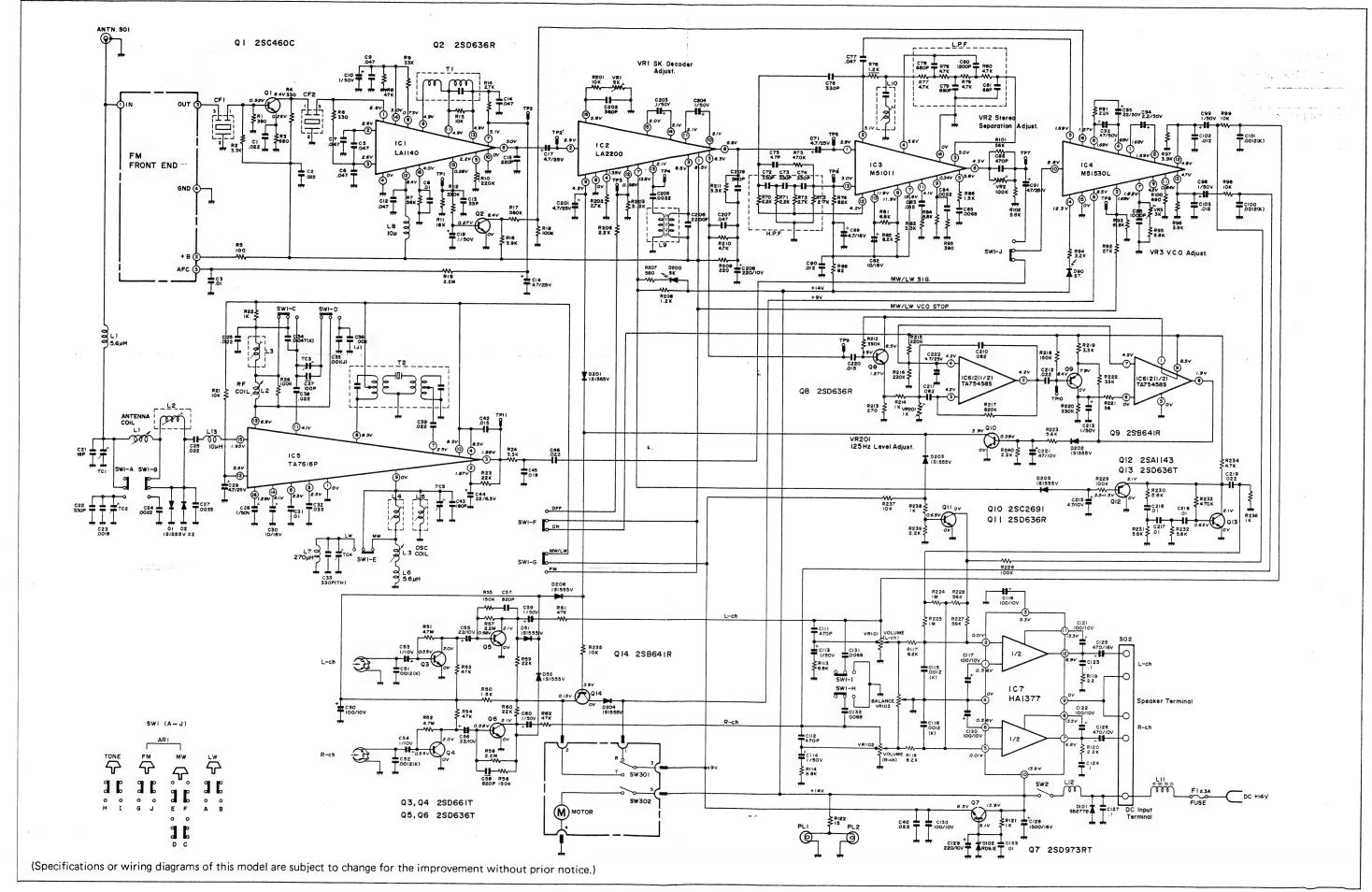


Figure 25 SCHEMATIC DIAGRAM (RG-5900H)

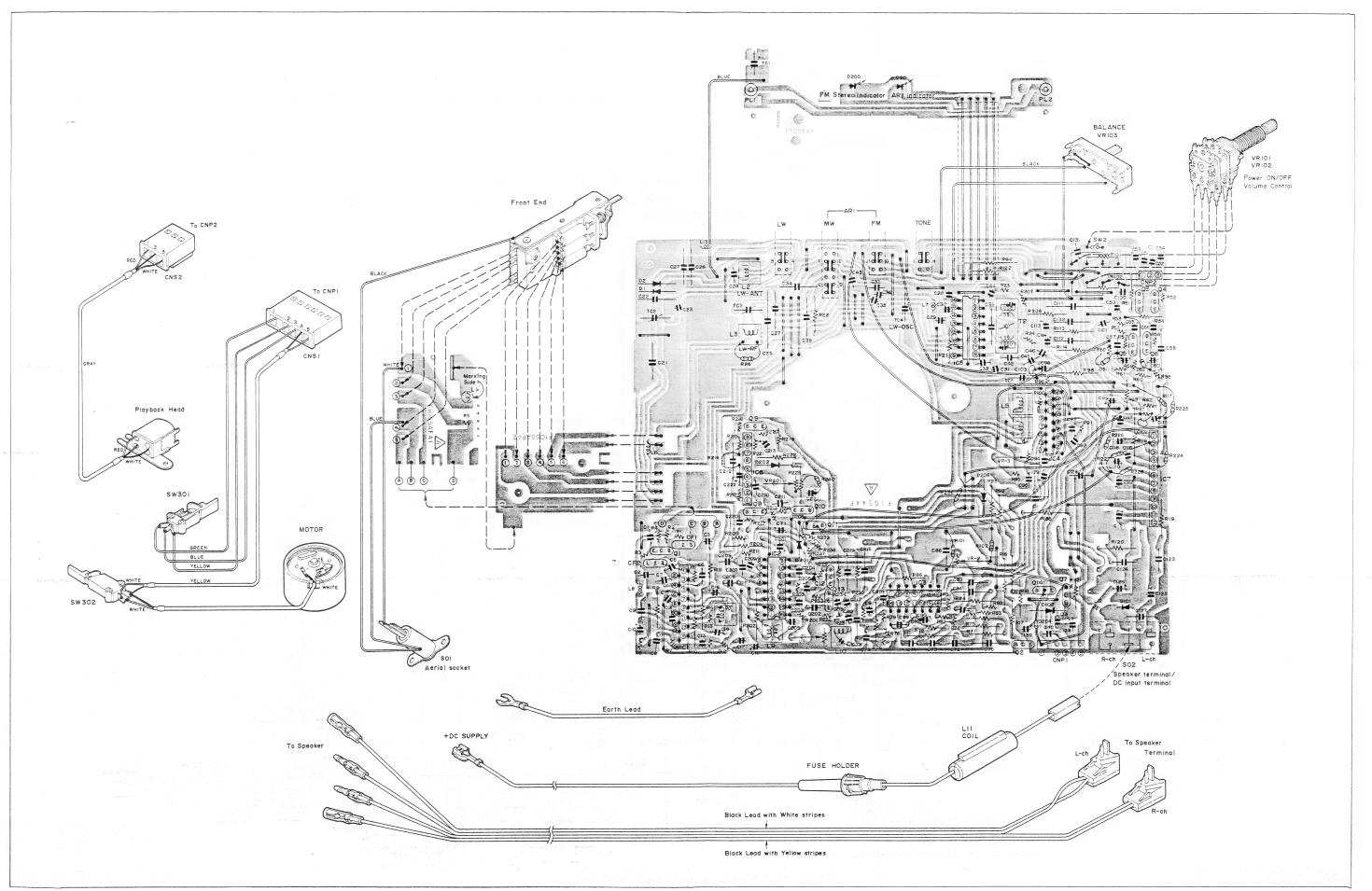


Figure 27 WIRING SIDE OF P.W. BOARD (RG-5900H)

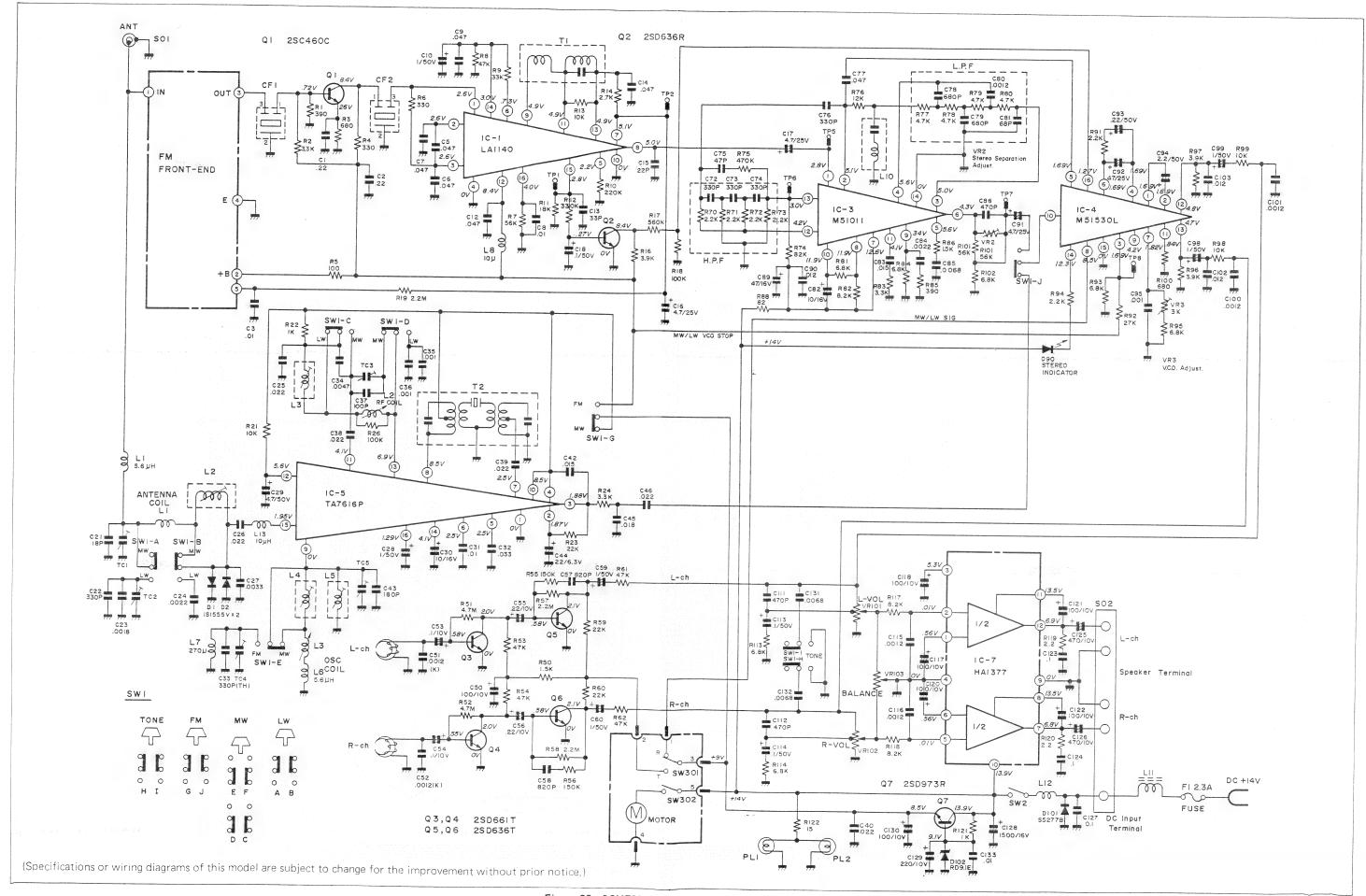


Figure 29 SCHEMATIC DIAGRAM (RG-5900E)

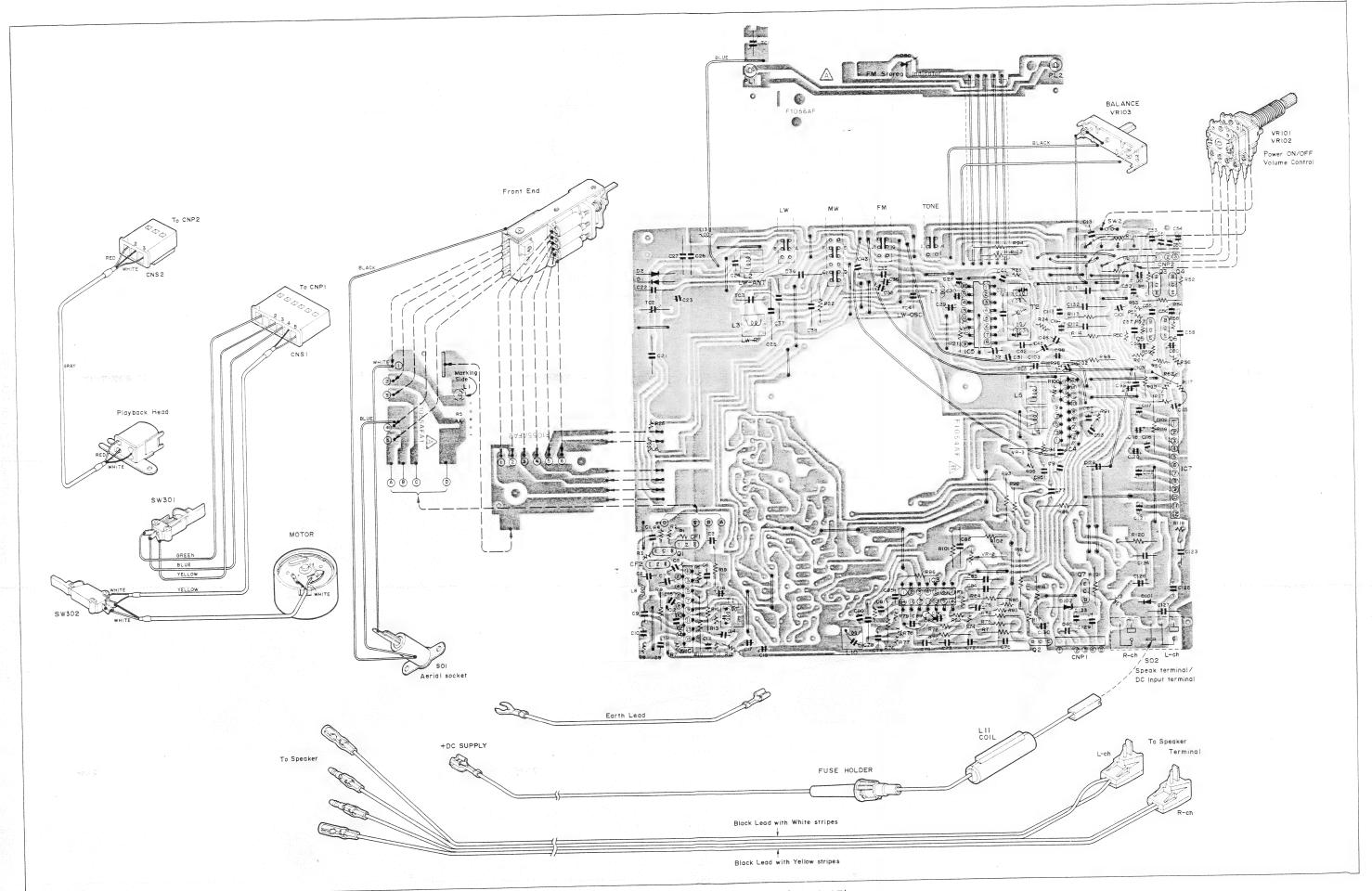


Figure 31 WIRING SIDE OF P.W. BOARD (RG-5900E)

-33-

-34-

-35-

Figure 36 MECHANISM EXPLODED VIEW

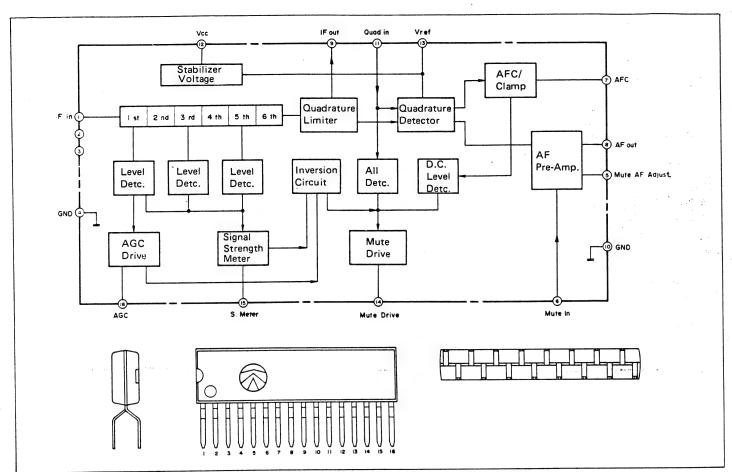


Figure 37-1 EQUIVALENT CIRCUIT OF INTEGRATED CIRCUITS (IC1)

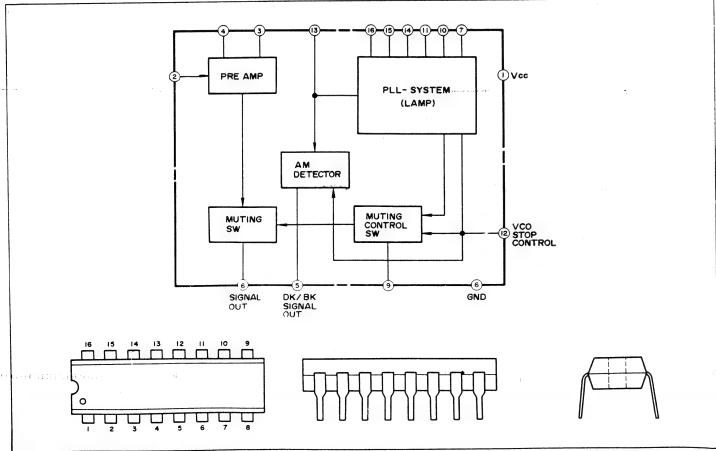


Figure 37-2 EQUIVALENT CIRCUIT OF INTEGRATED CIRCUITS (IC2)

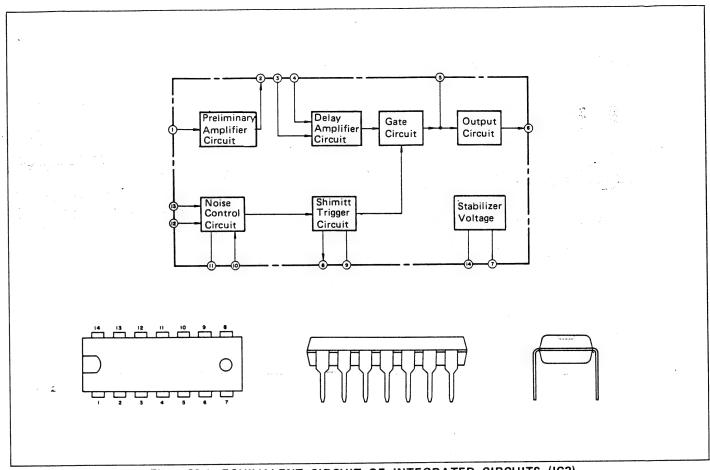
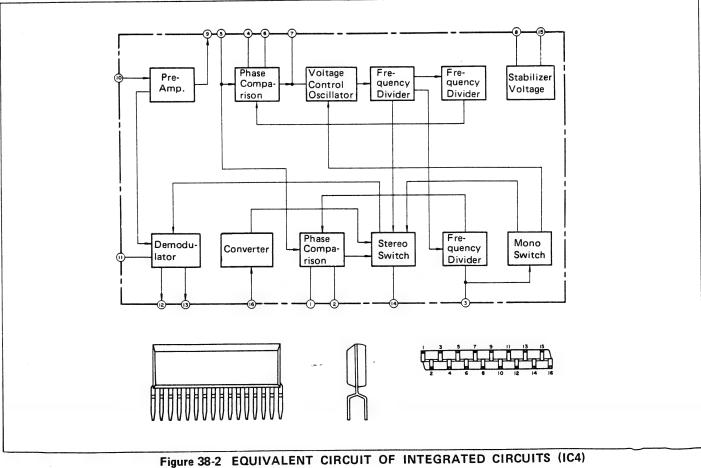


Figure 38-1 EQUIVALENT CIRCUIT OF INTEGRATED CIRCUITS (IC3)



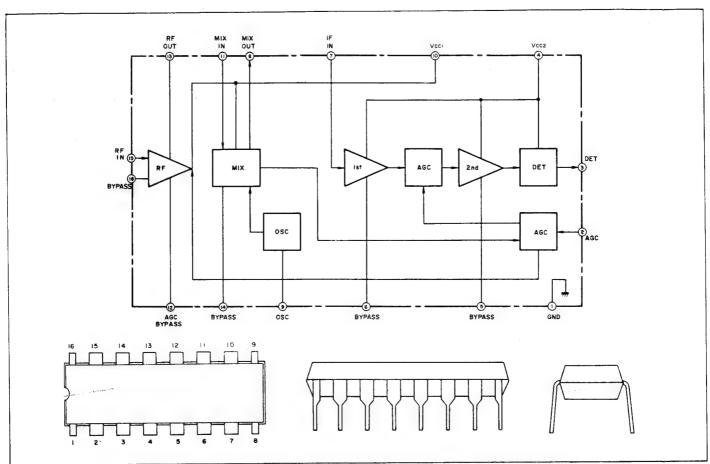


Figure 39-1 EQUIVALENT CIRCUIT OF INTEGRATED CIRCUITS (IC5)

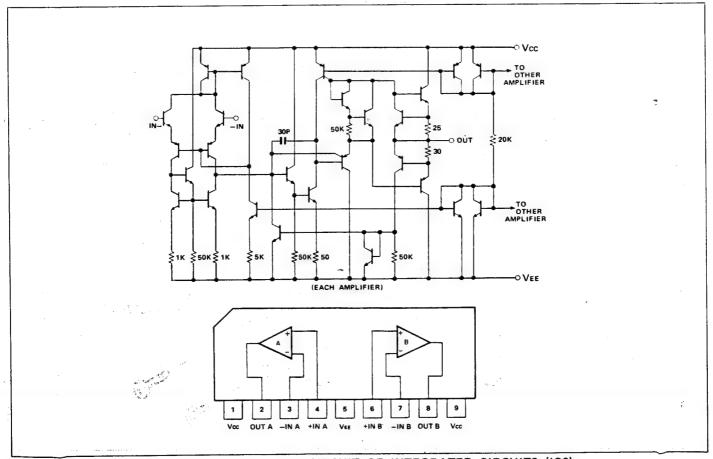


Figure 39-2 EQUIVALENT CIRCUIT OF INTEGRATED CIRCUITS (IC6)

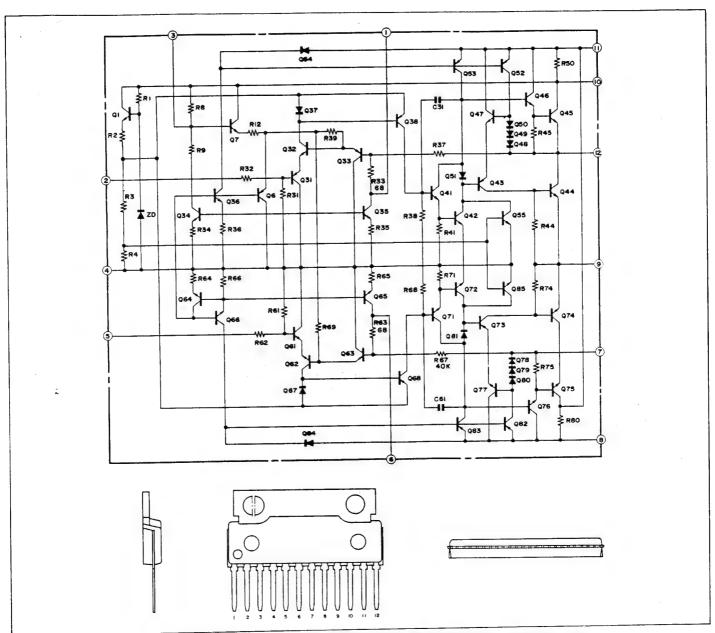


Figure 40-1 EQUIVALENT CIRCUIT OF INTEGRATED CIRCUITS (IC7)

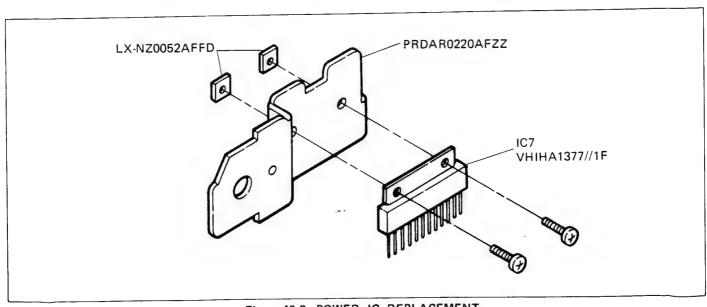


Figure 40-2 POWER IC REPLACEMENT

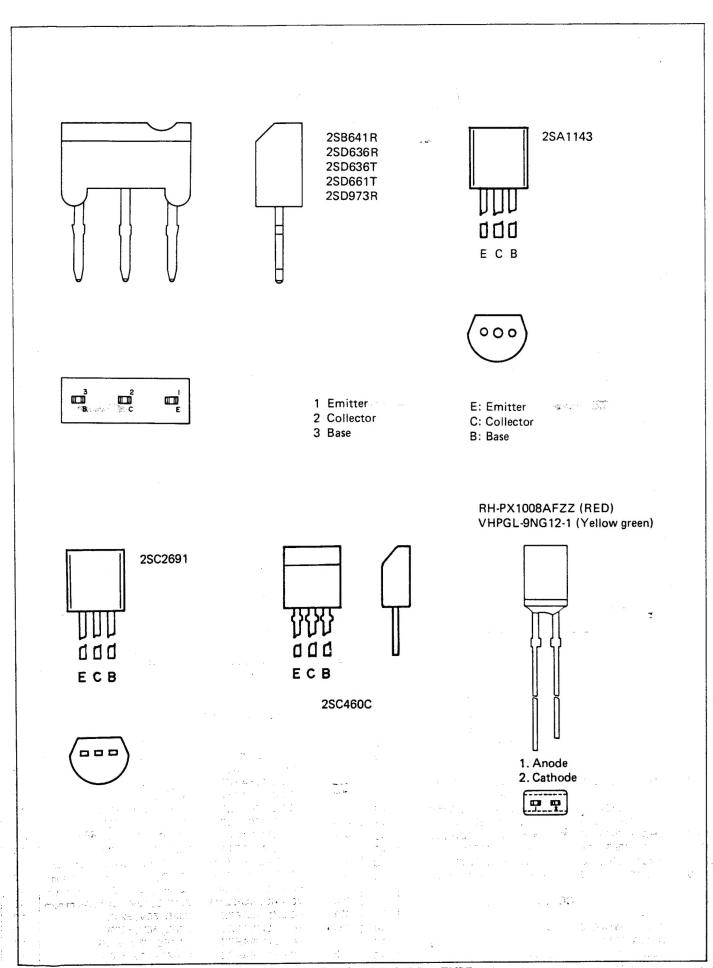


Figure 41 TRANSISTORS AND L.E.D.s TYPE

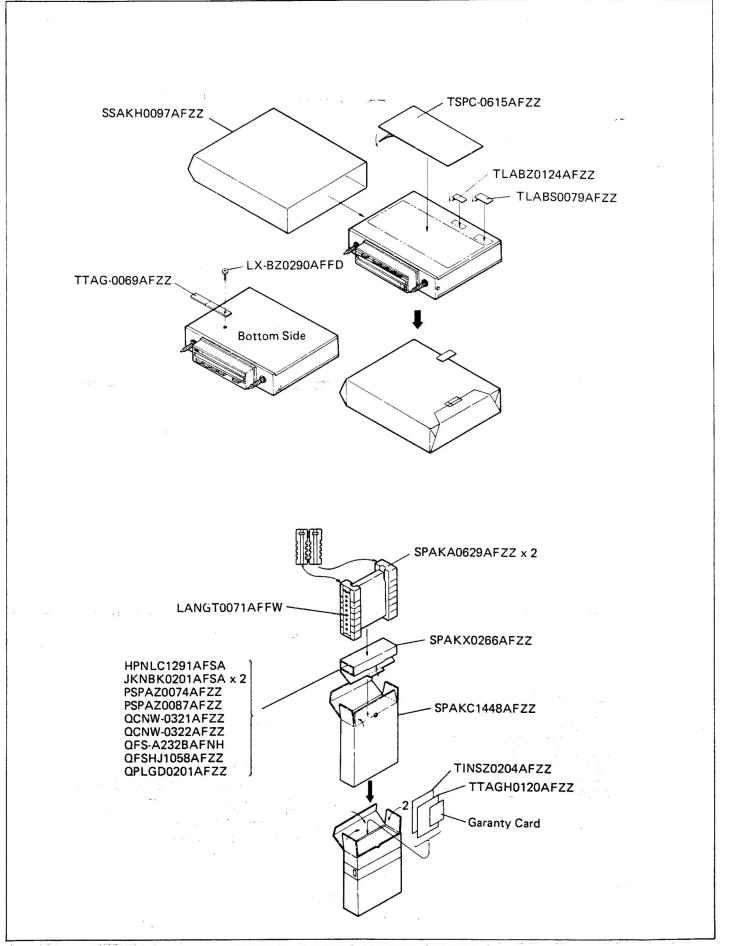
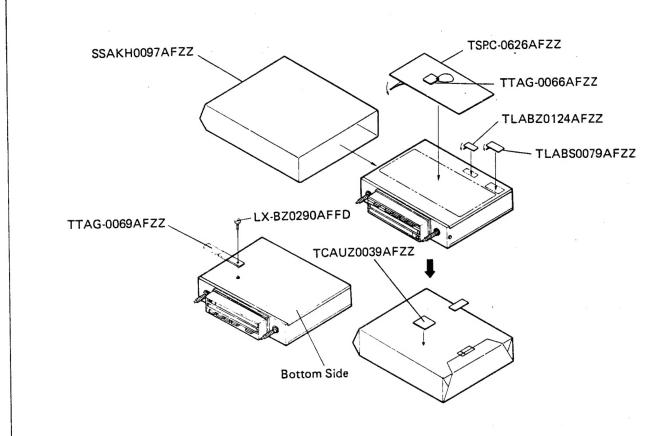


Figure 42 PACKING METHOD (RG-5900H)



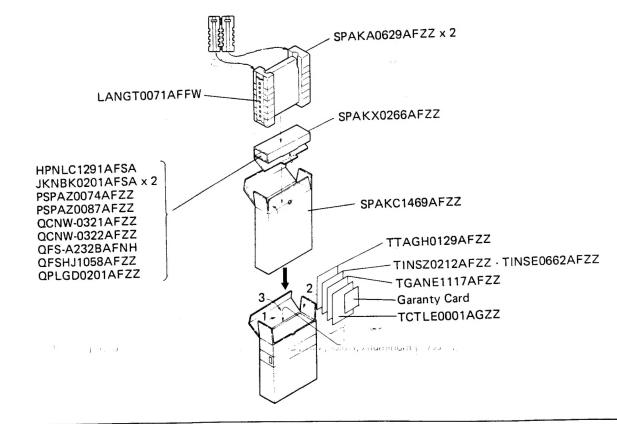


Figure 43 PACKING METHOD (RG-5900E)

REPLACEMENT PARTS LIST

"HOW TO ORDER REPLACEMENT PARTS"

To have your order filled promptly and correctly, please furnish the following information.

1. MODEL NUMBER

2. REF. NO.

3. PART NO.

4. DESCRIPTION

	REF. NO.	PART NO.	DESCRIPTION	CODE	REF. NO.	PART NO.	DESCRIPTION	CODE
		INTEGRAT	ED CIRCUITS		L6	RCILC0074AFZZ	MW Oscillator, 5.6µH	AB
					L7	RCILCO075AFZZ	LW Oscillator, 270µH	AB
	IC1	VH!LA1140//1F	FM IF Amp. (LA1140)	AK	L8	RCILC0077AFZZ	Choke	AE
	IC2	VHILA22002A-1	ARI Circuit (LA22002A)	AL	L9	RCILZ0076AFZZ	SK Decoder V.C.O.	AD
	IC3	VHIM51011//1F	ANSS Circuit (M51011)	AG	L10	RCILZ0085AFZZ	Trap, 19KHz	AE
	IC4	VHIM51530L/1F	FM M.P.X. (M51530L)	AK	L12	RCILF0067AFZZ	Choke	AC
	1C5	VHITA7616P/1F	AM RF/OSC/IF Detector	AK	L13	RCILC0077AFZZ	Noise	AE
			(TA7616P)					
	IC6	RH-IX1132AFZZ	125Hz B.P.F. and Schmitt Circuit (TA75458S)	AG			FORMERS	
	IC7	VHIHA1377//1F	Power Amplifier (HA1137)	AR.	T1 T2	RCILI0269AFZZ RFILA0007AFZZ	FM IF	AE AH
		z TRAN	ISISTORS				TERS	
						FIL	IENS	
	Q1	VS2SC460-C/-1	FM 1F Amp. (2SC460C)	AC	CF1,]	RFILF0055AFZZ	Ceramic Filter, 10.7MHz	AE
	Ω2	VS2SD636-R/-1	Switching, Separation Control Signal (2SD636R)	AD	CF2	NF1E1 0000A1 22	Coldinic Friedry, 10,714112	,,_
	Q3	VS2SD661-T/-1	1st Pre-amplifier (2SD661T)	AB		CON	TROLS	
	Q4	VS2SD661-T/-1	1st Pre-amplifier (2SD661T)	AB		CON	INOLO	
	Q5	VS2SD636-T/-1	2nd Pre-amplifier (2SD636T)	AB	TC1	RTO-A1057AFZZ	Trimmer, MW Antenna	AD
	Q6	VS2SD636-T/-1	2nd Pre-amplifier (2SD636T)	AB	TC2	RTO-A1058AFZZ	Trimmer, LW Antenna	AE
	Q7	VS2SD973-R/-1	Voltage Regulator (2SD973R)	AC	TC3	RTO-A1057AFZZ	Trimmer, RF	AD
	Ω8	VS2SD636-R/-1	Impedance Converter	AD	TC4	RTO-A1058AFZZ	Trimmer, LW Oscillator	AE
			(2SD636R)	4.0	TC5	RTO-A1057AFZZ	Trimmer, MW Oscillator	AD
	Q9	VS2SB641-R/-1	Rectifier (2SB641R)	AB	VR1	RVR-M0220AFZZ	5K ohm (B), SK Decorder	AB
	Q10	VS2SC2691//-1	DK Switching (2SC2691)	AB	VR2	RVR-M0223AFZZ	100K ohm (B), Stereo	AB
	Q11	VS2SD636-R/-1	DK Switching	AD			Separation Adjust.	
	.012	VS2SA1143//-1	Alarm Circuit (2SA1143)	AB	VR3	RVR-M0230AFZZ	3K ohm (B), V.C.O. Adjust.	AB
	Q13	VS2SD636-T/-1	Alarm Circuit (2SD636T)	AB AB	VR101,)	50K ohm (B) x 2, Volume	
	Q14	VS2SB641-R/-1	Switching (2SB641R)	AB	VR102	RVR-B0219AFZZ	Control Volume Assembly with Power Switch (SW2)	ÅМ
			10023		VR103	RVR-Q0073AFZZ	50K ohm (B), Balance Control	AF
	D1, D2	VHD1S1555V/1G	Static Protector (1S1555V)	AB	VR201	RVR-M0218AFZZ	1K ohm (B), 125Hz Level	AB
	D51	VHD1S1555V/1G	Muting Circuit (1S1555V)	AB			Adjust.	
	D52	VHD1S1555V/1G	Muting Circuit (1S1555V)	AB				
	D90	RH-PX1008AFZZ	FM Stereo Indicator (GL-9PR2) (Red)	AD		ELECTROLYT	IC CAPACITORS	
	D101	VHDS5277B//-1	Protector (S5277B)	AB	C10	RC-EZ1115AFZZ	1MFD,50V,±20%	AD
	D102	VHERD9.1ED/-1	Zener Diode (9.1V), Voltage	AB	C16	VCEAAU1EW475Y	4.7MFD, 25V, +50 -10%	AB
	•		Regulator (RD9.1E)		C17	VCEAAU1EW475Y	4.7MFD, 25V, +50 -10%	AB
	D200	VHPGL-9NG12-1	ARI Indicator (GL-9NG12)	AD	C18	RC-EZ1138AFZZ	.1MFD,50V,±20%	AB
			(Yellow-green)		C28	RC-EZ1115AFZZ	1MFD, 50V, ±20%	AD
	D201	VHD1S1555V/1G	Reverse Current (1S1555V)	AB	C29	RC-EZ1109AFZZ	4.7MFD, 25V, ±20%	AB
	D202	VHD1S1555V/1G	Protector (1S1555V)	AB	C30	RC-EZ1106AFZZ	10MFD, 16V, ±20%	AE
	D203	VHD1S1555V/1G	Reverse Current (1S1555V)	AB	C44	RC-EZ1100AFZZ	22MFD, 6.3V, ±20%	AB
	D204	VHD1S1555V/1G	Protector (1S1555V)	AB	C50	RC-EZS107AF1A	100MFD, 10V, ±20%	AB
	D205	VHD1S1555V/1G	Protector (1S1555V)	AB	C53	VCAAKU1AA105M	.1MFD, 10V, ±20%, Aluminum	AC
	D206	VHD1S1555V/1G	Reverse Current (1S1555V)	AB	C54	VCAAKU1AA105M	.1MFD, 10V, ±20%, Aluminum	AC
					C55	VCAAKU1AA224M	.22MFD, 10V, ±20%, Aluminum	AB
		(COILS		C56	VCAAKU1AA224M	.22MFD, 10V, ±20%, Aluminum	AB
					C59	RC-EZ1115AFZZ	1MFD,50V,±20%	AD
1	L1	RCILC0061AFZZ	Choke	AB	C60	RC-EZ1115AFZZ	1MFD, 50V, ±20%	AD
,	L2	RCILA0301AFZZ	Noise	AD	C71	RC-EZ1109AFZZ	4,7MFD, 25V, ±20%	AB
	L3	RCILA0301AFZZ	Choke	AD	C82	RC-EZ1106AFZZ	10MFD, 16V, ±20%	AE
	L4	RCILB0474AFZZ	LW Oscillator	AD	C89	RC-EZS476AF1C	47MFD, 16V, ±20%	AB
	L5	RCILB0475AFZZ	MW Oscillator	I AD	C91	VCEAAU1EW475Y	4.7MFD, 25V, +50 –10%	I AB

PARTS LIST

PARTS LIST

REF.	PART NO.	DESCRIPTION	CODE	REF. NO.	PART NO.	DESCRIPTION	CODE	REF. NO.	PART NO.	DESCRIPTION (Victor FP)	CODE	REF.	PART NO.	DESCRIPTION CODE
NO.	PART NO.	5 255111111011		NO.					VOTVDUIEV199V	.0012MFD, 25V, ±10%,		R53	VRD-SU2EE473J	47K ohm
C92	RC-EZ1114AFZZ	.47MFD,50V,±20%	AB	C32	VCTYPU1EX333M	.033MFD, 25V, ±20%,		C116	VCTYPU1EX122K	Semiconductor		R54	VRD-ST2EE473J	47K ohm
C92	RC-EZ1139AFZZ	.22MFD, 50V, ±20%	AB			Semiconductor		C123	VCQYKU1HM104I		AC	R55	VRD-SU2EE154J	150K ohm
C93	RC-EZ1116AFZZ	2.2MFD, 50V, ±20%	AB	C33	VCCTPU1HH331J	330PF, (TH), 50V, ±5%,		C123	VCQYKU1HM104		AC	R56	VRD-SU2EE154J	150K ohm
C94	RC-EZ1115AFZZ	1MFD,50V,±20%	AD			Ceramic		C124	VCKZPU1HF104Z			R57	VRD-SU2EE225J	2.2Meg ohm
C99	RC-EZ1115AFZZ	1MFD,50V,±20%	AD	C34	VCTYAT1HV472K	.0047MFD,50V,±10%,		C127	VCKZI OTTI TOTA	Ceramic		R58	VRD-SU2EE225J	2.2Meg ohm
C113	RC-EZ1138AFZZ	.1MFD,50V,±20%	AB			Semiconductor		C131	VCTYPU1EX682K	.0068MFD, 25V, ±10%,		R59	VRD-SU2EE223J	22K ohm
C114	RC-EZ1138AFZZ	.1MFD,50V,±20%	AB	C35	VCTYPU1EX102J	.001MFD, 25V, ±5%, Semiconductor				Semiconductor		R60	VRD-SU2EE223J	22K ohm
C117	RC-EZS107AF1A	100MFD, 10V, ±20%	AB			.001MFD, 25V, ±5%,		C132	VCTYAT1EX682K	.0068MFD, 25V, ±10%,		R61	VRD-SU2EE473J	47K ohm
C118	RC-EZS107AF1A	100MFD, 10V, ±20%	AB	C36	VCTYPU1EX102J	Semiconductor				Semiconductor		R62	VRD-SU2EE473J	47K ohm 2,2K ohm
C120	RC-EZS107AF1A	100MFD, 10V, ±20%	AB	007	VCCSAT1HL101J	100PF , 50V , ±5% , Ceramic		C133	VCTYPU1EX103N			R70	VRD-ST2EE222J VRD-ST2EE222J	2.2K ohm
C121	RC-EZS107AF1A	100MFD, 10V, ±20%	AB	C37	VCTYAT1CY223N	.022MFD, 16V, ±30%,				Semiconductor	1	R71	VRD-STZEE222J VRD-SU2EE272J	2.7K ohm
C122	RC-EZS107AF1A	100MFD, 10V, ±20%	AB AC	C38	VCT FAT ICT 225N	Semiconductor		C202	VCQSMU1HS361J		AB	R72 R73	VRD-ST2EE273J	27K ohm
C125	RC-EZS477AF1A	470MFD, 10V, ±20%	AC	C39	VCTYAT1CY223N	.022MFD, 16V, ±30%,		C205	VCTYPU1EX222N			R73	VRD-SU2EE823J	82K ohm
C126	RC-EZS477AF1A	470MFD, 10V, ±20%	AE	C39	VCTTATTOTZZON	Semiconductor				Semiconductor	AB	R75	VRD-ST2EE474J	470K ohm
C128	RC-EZ1075AFZZ	1500MFD, 16V, ±20%	AB	C40	VCTYPU1EX223M	.022MFD , 25V , ±20% ,		C206	VCQSMU1HS222J	.0022MFD, 50V, ±5%, Styrol	AB	R76	VRD-ST2EE122J	1,2K ohm
C129	RC-EZS227AF1A	220MFD, 10V, ±20% 100MFD, 10V, ±20%	AB	C40	VC11101=112=0	Semiconductor		C207	VCTYPU1EX473N			R77	VRD-SU2EE472J	4.7K ohm
C130	RC-EZS107AF1A	4.7MFD, 25V, +50 -10%	AB	C42	VCTYPU1EX153K	.015MFD, 25V, ±10%,				Semiconductor 560PF, 50V, ±5%, Ceramic		R78	VRD-SU2EE472J	4.7K ohm
C201	VCEAAU1EW475Y	.1MFD, 50V, ±20%	AB	0,12		Semiconductor		C209			1	R79	VRD-SU2EE472J	4.7K ohm
C203	RC-EZ1138AFZZ RC-EZ1115AFZZ	1MFD,50V,±20%	AD	C43	VCCRPU1HH181J	180PF,(RH),50V,±5%,		C210	VCQYKU1HM823 VCQYKU1HM823			R80	VRD-SU2EE472J	4.7K ohm
C204 C208	RC-EZS227AF1A	220MFD, 10V, ±20%	AB		** *	Ceramic		C211				R81	VRD-SU2EE682J	6.8K ohm
C208	RC-EZ1115AFZZ	1MFD.50V, ±20%	AD	C45	VCTYPU1EX183K	.018MFD, 25V, ±10%,		C212	VC YATTC 1223	Semiconductor		R82	VRD-SU2EE822J	8.2K ohm
C215	RC-EZ1116AFZZ	2.2MFD, 50V, ±20%	AB			Semiconductor		6216	VCTYAT1EX103	-		R83	VRD-SU2BB332J	3.3K ohm, 1/8W, ±5%, Carbon
C213	RC-EZ1105AFZZ	47MFD, 10V, ±20%	AB	C46	VCTYPU1EX223M	.022MFD, 25V, ±20%,		C216	VCITALIEXIOSI	Semiconductor		R84	VRD-ST2EE682J	6.8K ohm
C222	RC-EZ1109AFZZ	4.7MFD, 25V, ±20%	AB			Semiconductor		C217	VCTYPU1EX103N	•		R85	VRD-SU2BB391J	390 ohm, 1/8W, ±5%, Carbon
CZZZ	110 22 7 700 11			C51	VCTYPU1EX122K	.0012MFD, 25V, ±10%,		C217	VCTTTOTEXTOO	Semiconductor		R86	VRD-ST2EE152J	1.5K ohm
	CAPA	ACITORS				Semiconductor		C218	VCTYPU1EX103			R88	VRD-ST2EE820J	82 ohm
				C52	VCTYPU1EX122K	.0012MFD, 25V, ±10%,		OZ 10	, , , , , , , , , , , , , , , , , , , ,	Semiconductor		R91	VRD-SU2EE222J	2.2K ohm
C1	VCTYPU1EX223M	.22MFD, 25V, ±20%,				Semiconductor 820PF . 50V . ±5% , Ceramic		C219	VCTYAT1CY223	.022MFD, 16V, ±30%,		R92	VRD-ST2EE273J	27K ohm
		Semiconductor		C57	VCRYPU1HB821J	820PF, 50V, ±3%, Ceramic				Semiconductor		R93	VRD-ST2EE682J	6.8K ohm
C2	VCTYPU1EX223M	.22MFD, 25V, ±20%,		C58	VCKYAT1HB821K VCRYPU1HB331J	330PF, 50V, ±5%, Ceramic		C220	VCTYPU1EX153	и .015MFD, 25V, ±20%,		R94	VRD-ST2EE222J	2,2K ohm
		Semiconductor		C72 C73	VCRYPU1HB331J	330PF , 50V , ±5% , Ceramic				Semiconductor		R95	VRD-SU2EE682J	6.8K ohm 3,9K ohm
C3	VCTYPU1EX103M	.01MFD, 25V, ±20%,		C74	VCRYPU1HB331J	330PF, 50V, ±5%, Ceramic						R96	VRD-SU2EE392J	3.9K ohm
		Semiconductor		C75	VCCSAT1HL4R7C	4.7PF, 50V, ±0.25PF, Ceramic			R	ESISTORS		R97	VRD-SU2EE392J VRD-ST2EE103J	10K ohm
C5	VCTYPU1EX473M	.047MFD, 25V, ±20%,		C76	VCRYPU1HB331J	330PF, 50V, ±5%, Ceramic _						R98	VRD-S12EE103J	10K ohm
		Semiconductor		C77	VCTYPU1EX473M	.047MFD, 25V, ±20%,		(Unk	ess otherwise specified r	esistors are 1/4W, ±5%, Carbon type.)		R99 R100	VRD-SU2EE681J	680 ohm
C6	VCTYPU1EX473M	.047MFD, 25V, ±20%,			* • *	Semiconductor	P 45.	11		200		R101	VRD-ST2EE563J	56K ohm
G		Semiconductor .047MFD, 25V, ±20%,		C78	VCKYAT1HB681K	680PF,50V,±10%, Ceramic		R1	VRD-SU2EE391J	390 ohm		R102	VRD-ST2EE562J	5.6K ohm (RG-5900H)
C7	VCTYPU1EX473M	Semiconductor		C79	VCRYPU1HB681J	680PF , 50V , ±5% , Ceramic		R2	VRD-SU2EE332J	3.3K ohm		R102	VRD-ST2EE682J	6.8K ohm (RG-5900E)
-	MATARIA EVIDAM	.01MFD, 25V, ±20%,		C80	VCTYPU1EX122M	.0012MFD, 25V, ±20%,		R3	VRD-SU2EE681J	680 ohm 330 ohm		R113	VRD-ST2EE682J	6.8K ohm
C8	VCTYPU1EX103M	Semiconductor				Semiconductor		R4	VRD-SU2EE331J VRD-SU2EE101J	100 ohm		R114	VRD-ST2EE682J	6.8K ohm
CO	VCTYPU1EX473M	.047MFD, 25V, ±20%,		C81	VCCSPU1HL680J	68PF, 50V, ±5%, Ceramic		R6	VRD-SU2EE331J	330 ohm		R117	VRD-ST2EE822J	8.2K ohm
C9	VCTTFOTEX475W	Semiconductor		C83	VCTYAT1EX153N	.015MFD, 25V, ±30%,		R7	VRD-SU2EE563J	56K ohm		R118	VRD-ST2EE822J	8.2K ohm
C12	VCTYPU1EX473M	.047MFD, 25V, ±20%,				Semiconductor		R8	VRD-SU2EE473J	47K ohm		R119	VRD-SU2EE2R2J	2.2 ohm
CIZ	VOLITIONEX	Semiconductor		C84	VCTYAT1EX222N	.0022MFD, 25V, ±30%,		R9	VRD-ST2EE333J	33K ohm		R120	VRD-ST2EE2R2J	2.2 ohm
C13	VCCSPU1HL330J	33PF, 50V, ±5%, Ceramic		-05	VOONKIITIMEOOK	Semiconductor .0068MFD, 50V, ±10%, Mylar	AA	R10	VRD-ST2EE224J	220K ohm		R121	VRD-ST2EE102J	1K ohm
C14	VCTYPU1EX473M	.047MFD, 25V, ±20%,		C85	VCQYKU1HM682K VCKYAT1HB471K	470PF, 50V, ±10%, Ceramic	/ " '	R11	VRD-SU2EE183J	18K ohm		R122	VRD-ST2EE150J VRD-SU2EE103J	15 ohm 10K ohm
		Semiconductor		C86	VCTYPU1EX123M	.012MFD, 25V, ±20%,		R12	VRD-SU2EE334J	330K ohm		R201 R202	VRD-ST2EE272J	2.7K ohm
C15	VCRYPU1HB221J	22PF , 50V , ±5% , Ceramic		C90	VCT TPOTEXT25W	Semiconductor		R13	VRD-SU2EE103J	10K ohm		R202	VRD-SU2EE332J	3.3K ohm
C21	VCCSAT1HL180J	18PF, 50V, ±5%, Ceramic		C95	VCQSMU1HS102J	.001MFD, 50V, ±5%, Styrol	AB	R14	VRD-ST2EE272J	2.7K ohm		R206	VRD-ST2EE222J	2.2K ohm
C22	VCKYAT1HB331K	330PF, 50V, ±10%, Ceramic		C100	VCTYPU1EX122K	.0012MFD, 25V, ±10%,		R16		3.9K ohm		R207	VRD-ST2HD561J	560 ohm, 1/2W, ±5%, Carbon
C23	VCTYPU1EX182J	.0018MFD, 25V, ±5%,		0.00		Semiconductor		R17	VRD-ST2EE564J	560K ohm		R208	VRD-ST2EE122J	1.2K ohm
		Semiconductor		C101	VCTYPU1EX122K	.0012MFD, 25V, ±10%,		R18	VRD-ST2EE104J	100K ohm		R209	VRD-SU2EE221J	220 ohm
C24	VCTYPU1EX222K	.0022MFD, 25V, ±10%, Semiconductor				Semiconductor		R19	VRD-ST2EE225J VRD-ST2EE103J	2,2Meg ohm 10K ohm		R210	VRD-SU2EE472J	4.7K ohm
				C102	VCTYPU1EX123K	.012MFD, 25V, ±10%,		R21	VRD-ST2EE 103J	1K ohm		R211	VRD-SU2EE332J	3.3K ohm
C25	VCTYAT1CY223N	Semiconductor	1			Semiconductor		R22		22K ohm	52	R212	VRD-SU2EE394J	390K ohm
	VCTYAT1CY223N	.022MFD, 16V, ±30%,		C103	VCTYPU1EX123K	.012MFD, 25V, ±10%,		R23		3.3K ohm	1	R213	VRD-SU2EE271J	270 ohm
C26	VCITATICT223N	Semiconductor	1.4			Semiconductor		R26				R214	VRD-SU2EE102J	1K ohm
C27	VCTYAT1EX332N		1	C111	VCKYAT1HB471K	470PF, 50V, ±10%, Ceramic		R50		1.5K ohm		R215	VRD-SU2EE224J	220K ohm
027	VO. 1741 127400214	Semiconductor		C112	VCKYAT1HB471K	470PF, 50V, ±10%, Ceramic		R51	VRD-ST2EE475J	4.7Meg ohm	1	R216	VRD-SU2EE224J	220K ohm
C31	VCTYPU1EX103M	.01MFD, 25V, ±20%,		C115	VCTYPU1EX122K	.0012MFD, 25V, ±10%,		R52		4.7Meg ohm		R217	VRD-SU2EE824J	820K ohm
		On the advance	1	1		Semiconductor	1	1						

PARTS LIST

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REF. NO.	PART NO.	DESCRIPTION	CODE	REF. NO.	PART NO.	DESCRIPTION	CODE
R218	VRD-SU2EE154J	150K ohm		034	MSPRT0653AFFJ	Spring, Switch Lock Lever	AA
R219	VRD-SU2BB332J	3.3K ohm, 1/8W, ±5%, Carbon		035	MSPRT0660AFFJ	Spring, Pinch Roller	AA
R220	VRD-SU2EE334J	330K ohm		036	MSPRT0671AFFJ	Spring, Sensor Lever	'"
R221	VRD-SU2EE560J	56 ohm	1 1	037	NBLTK0161AFZZ	Belt, Flywheel Drive	AC
R222	VRD-ST2EE333J	33K ohm		038	NBLTK0162AFZZ	Belt, Auto Stop	AB
R223	VRD-ST2EE562J	5.6K ohm		039	NDAIR0143AFSA	Turntable, Take-up	AK
R224	VRD-ST2BB105J	1Meg ohm, 1/8W, ±5%, Carbon		040	NDAIR0144AFSA	Supply Reel	AE
R225	VRD-ST2BB105J	1Meg ohm, 1/8W, ±5%, Carbon		041	NFLYC0084AFZZ	Flywheel	AH
R226	VRD-ST2BB104J	100K ohm, 1/8W, ±5%, Carbon		042	NIDR-0071AFZZ	Play Idler Assembly	AF
R227	VRD-ST2BB563J	56K ohm, 1/8W, ±5%, Carbon		043	NROLP0061AFZZ	Idler Gear	AA
R228	VRD-ST2BB563J	56K ohm, 1/8W, ±5%, Carbon		044	NROLP0062AFZZ	Idler Gear	AA
R229	VRD-SU2EE104J	100K ohm		045	NROLP0063AFZZ	Gear, Cam	AB
R230	VRD-ST2EE562J	5.6K ohm		046	NROLV0015AFZZ	Fast Forward Roller Assembly	AG
R231	VRD-SU2EE562J	5.6K ohm		047	NROLY0035AFZZ	Pinch Roller Assembly	AE
R232	VRD-SU2EE562J	5.6K ohm		048	NSFTT0142AFFD	Shaft, Sub Chassis Retaining	AB
R233	VRD-SU2EE474J	470K ohm		050	PGIDM0079AFSA	Cassette Guide	AC
R234	VRD-ST2EE472J	4.7K ohm		051	RHEDF0055AFZZ	Playback Head	AN
R235	VRD-ST2EE103J	10K ohm		052	PZETF0153AFZZ	Insulator	AB
R236	VRD-ST2BB102J	1K ohm, 1/8W, ±5%, Carbon		053	PGUMS0145AF00	Cushion Rubber	75
R237	VRD-SU2EE103J	10K ohm		000	1 GONSO 140A1 00	The second secon	
R238	VRD-SU2EE102J	1K ohm			MISCEL	LANEOUS	
R239	VRD-SU2EE221J	220 ohm		404			
R240	VRD-ST2BB222J	2.2K ohm, 1/8W, ±5%, Carbon		101	GCABA3526AFFW	Cabinet, Rear (Large)	AE
				102	GCABB3526AFFW	Cabinet, Front	AE
	MECHAN	ICAL PARTS		103	GCABC3526AFFW	Cabinet, Bottom	AD
				104	GCABD3526AFFW	Cabinet, Top	AF
001	LANGF0555AFZZ	Bracket, Flywheel	АВ	105	GFTAC3065AFSA	Cassette Compartment	AD
002	LANGH0132AFZZ	Bracket, Strengthening	~	106	GWAKP1084AFSA	Nose Piece (RG-5900H)	AG
003	LCHSM0333AFZZ	Main Chassis			GWAKP1085AFSA	Nose Piece (RG-5900E)	AG
004	LCHSM0334AFZZ	Sub Chassis		107	HDALP0439AFSA	Dial Plate (RG-5900H)	AE
005	LCHSS0153AFZZ	Head Base			HDALP0441AFSA	Dial Plate (RG-5900E)	
006	LHLDW1075AFZZ	Wire Holder, 60mm	AA	108	HPNLC1291AFSA	Operation Panel	AG
007	LHLDW3056AFZZ	Wire Holder	AA	109	HSSND0264AFSA	Dial Pointer	AC
008	LSLVM0089AFFW	Roller, Sub Chassis	AB	110	JKNBK0201AFSA	Knob, Power Switch/Volume	AD
009	LSLVM0090AFFW	Spacer, Fast Forward Roller	~			Control	
010	LX-BZ0219AFFD	Lever Screw, Switch ON/OFF Lever	AA	111	JKNBM0344AFSA	Knob, Cassette Eject Button/ Release Button for Fast	AB
		Retaining		110	IKNIDA 400 45 A 50 A	Forward Winding	
011	LX-WZ5015AGZZ	Washer, 3.1W6-0.25	AA	112	JKNBM0345AFSA	Knob, Tone Control/Band	AB
012	LX-WZ5018AGZZ	Washer, 2.1W4-0.25	AA	113	UKNIBBO100A FCA	Selector	
013	LX-WZ5020AGZZ	Washer, 1.7W3.2-0.25	AA		JKNBP0103AFSA	Knob, Balance Control	AB
014	LX-WZ9057AFZZ	Pad, Flywheel Bracket	AA	114	HDAP-0185AF00	Backplate, Dial, Black	AB
015	LX-WZ9064AFZZ	Washer, 1.5W3.8-0.5	AA	115	L ANO DOCO1 A EEU	(RG-5900E Only)	
016	LX-WZ9066AFZZ	Washer, 1.2W3.2-0.5	AA	115	LANGROSO1AFFW	Bracket, Tuner Unit	AC
017	MLEVF1037AFZZ	Lever, Operation	AF	116	LANGT0071AFFW	Plate, Back Strap	AB
018	MLEVF1038AFZZ	Lever, Eject	AD	117	LHLDF1235AF00	Holder, P.W. Board Retaining	AE
019	MLEVF1039AFZZ	Lever, Sub Chassis Guide	AB	118	LSTWC4004AFZZ	Stop Ring, Tuning Shaft	
020	MLEVF1040AFZZ	Lever, Sensor	AC	119	LX-LZ0051AF00	Push Rivet, P.W. Board	
021	MLEVF1041AFZZ	Lever, Play/Fast Forward Lock	AB	400		Retaining	
022	MLEVF1042AFZZ	Lever, Switch Lock	AB	120	LX-NZ0052AFFD	Nut, Heat Sink Retainer	AA
023	MLEVP0188AFZZ	Lever, Switch ON/OFF	AB	121	MRODM0071AFFW	Rod, Cassette Compartment	AA
024	MSPRC0210AFFJ	Spring, Head Azimuth Adjust	AA			Retaining	
025	MSPRC0212AFFJ	Spring, Supply Reel	AA	122	MSPRD0261AFFJ	Spring, Cassette Compartment	AA
026	MSPRD0266AFFJ	Spring, Play/Fast Forward Lock	AB	123	MSPRT0321AFFJ	Spring, Dial Cord	AA
		Lever		124	NGERHOO58AFZZ	Idler Gear, Tuning Shaft	AC
027	MSPRD0268AFFJ	Spring, Switch ON/OFF Lever	АВ	125	NGERHOO59AFZZ	Idler Gear, Tuning Shaft	AB
028	MSPRT0647AFFJ	Spring, Head Base Return	AA	126	NSFTZ0072AFFW	Tuning Shaft	AE
029	MSPRT0648AFFJ	Spring, Sub Chassis	AA	127	PCOVU3125AFFW	Holder, Lamp (Right)	AB
030	MSPRT0649AFFJ	Spring, Operation Lever	AA	128	PCOVU3126AFFW	Holder, Lamp (Left)	AB
-		Return	"	129	PCOVZ8055AFZZ	Cover, Lamp	AA
031	MSPRT0650AFFJ	Spring, Play Idler	AA	130	PFLT-0408AF00	Felt, Balance Control	AA
032	MSPRT0651AFFJ	Spring, Fast Forward Roller	AA	131	PRDAR0220AFZZ	Heat Sink	AF
		Lever	"	132	PSPAZ0074AFZZ	Spacer, Operation Panel	AD
033	MSPRT0652AFFJ	Spring, Eject Lever	AA	132-1	Not Available	Spacer, Part of PSPAZ0074AFZZ].

PARTS LIST

REF. NO.	PART NO.	DESCRIPTION	CODE	REF.	PART NO.	DESCRIPTION	CODE
133	PSPAZ0087AFZZ	Spacer Unit (Screw, Nut,	AD	141	RTUNC0135AFZZ	Tuner Unit Assembly	AY
		Washer)			SPAKA0629AFZZ	Packing Add	AC
133-1	Not Available	Nut, Part of PSPAZ0087AFZZ			SPAKC1448AFZZ	Packing Case (RG-5900H)	AD
133-2	Not Available	Washer, Part of			SPAKC1469AFZZ	Packing Case (RG-5900E)	AD
		PSPAZ0087AFZZ			SPAKX0266AFZZ	Case, Accessory Parts	AB
133-3	Not Available	Spacer, Part of			SPAKX0310AFZZ	Packing Add, Bottom Side	
		PSPAZ0087AFZZ			SSAKH0097AFZZ	Polyethiene Bag, Set	AA
134	PZETF0151AFZZ	Insulator, Bottom Cover (Large)	AD		TINSE0662AFZZ	Operation Manual	
135	PSLDM3181AFZZ	Insulator, Top Cover				(English Only, RG-5900E)	
CNP1	QCNCM0503SGZZ	Plug, 5 Pin	AC		TINSZ0204AFZZ	Operation Manual (RG-5900H)	AF
CNP2	QCNCM136CAFZZ	Plug, 3 Pin	AB		TINSZ0212AFZZ	Operation Manual (English/	
136	QCNW-0321AFZZ	Speaker Lead	AP			German/French/Swedish,	
137	QCNW-0322AFZZ	Earth Lead	AC			RG-5900E)	1
138	QCNW-0707AFZZ	Flat Cable, 5 Pin	AB		TLABS0079AFZZ	Label, F Mark	
CNS1	QCNW-0708AFZZ	Socket, 5 Pin with Wire Leads	AE		TLABZ0124AFZZ	Label, ANSS	
CNS2	QCNW-0724AFZZ	Socket, 3 Pin with Wire Leads	AE		TSPC-0615AFZZ	Specifications (RG-5900H)	AC
F1	QFS-A232BAFNH	Fuse, 2.3A	AC		TSPC-0626AFZZ	Specifications (RG-5900E)	AC
139 L11	QFSHJ1058AFZZ	DC Supply Lead (With Coil, Fuse Holder and Socket)	AL		TTAGH0120AFZZ	Tag, English/German/French/ Swedish (RG-5900H)	
140	QPLGD0201AFZZ	Speaker Plug Assembly	AC		TTAGH0129AFZZ	Tag, English/German/French/	
SO2	QSOCD0271AFZZ	Terminal, Speaker/DC Input	AG			Swedish (RG-5900E)	
SO1	QSOCZ0015AFZZ	Aerial Socket	AC				
SW30	1 QSW-F0141AFZZ	Switch, Tape/Radio Selector	AD				
SW30	2 QSW-F0141AFZZ	Switch, Motor ON/OFF	AD				
SW1	QSW-P0258AFZZ	Switch, Band Selector and Tone Control	AM	Р.	W.BOARD ASSEMBI	LY (Not Replacement Item)	
SW2	Not Available	Switch, Power (Part of VR101, VR102)			DUNTK0053AF02	Main/Sub P.W.Board (RG-5900H)	
PL1 I	PL2 RLMPM0069AFZZ	Lamp, Dial Illumination	AD		DUNTK0053AF04	Main/Sub P.W.Board	
MO30		Moter	AU			(RG-5900E)	